

[illegible]

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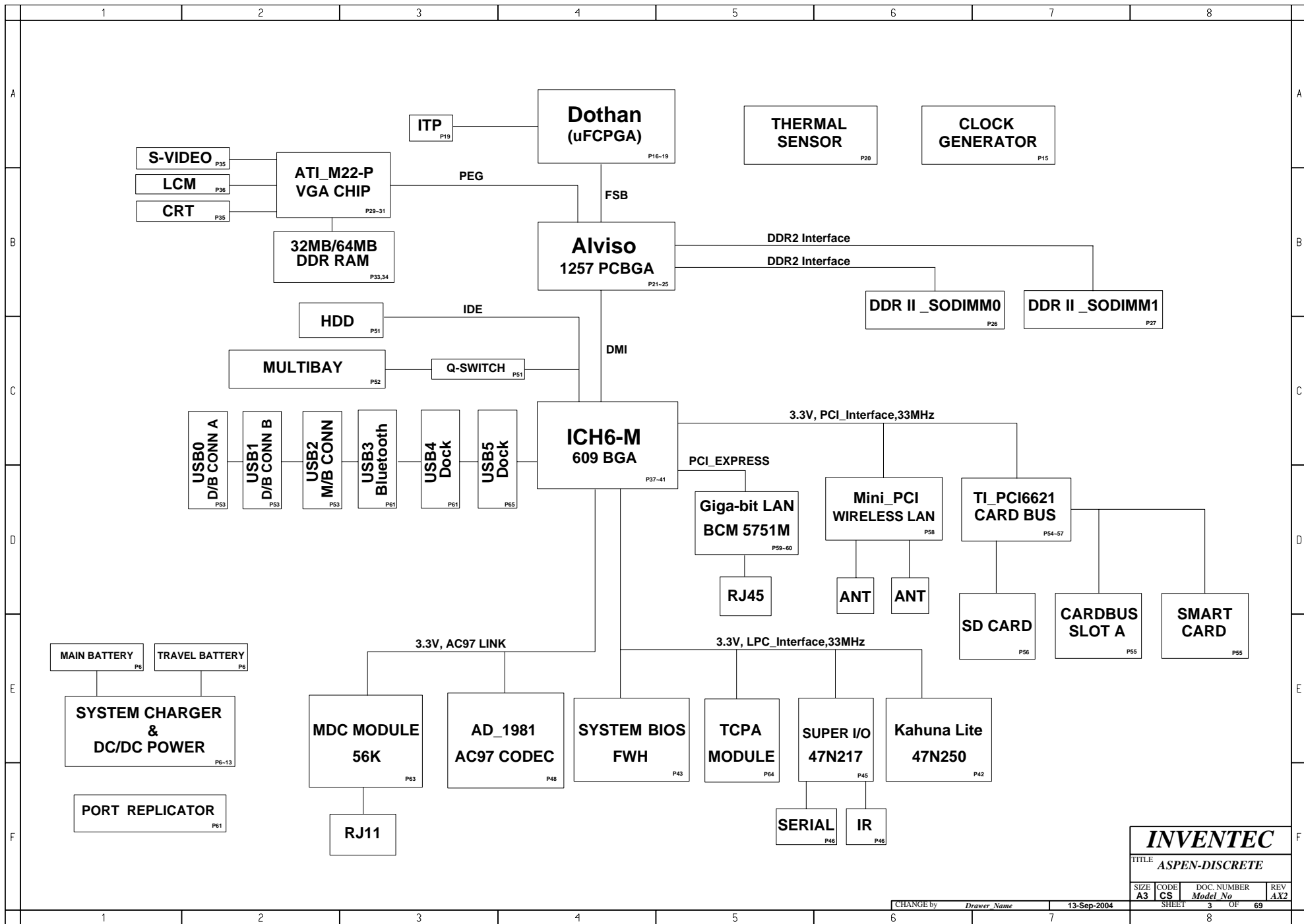
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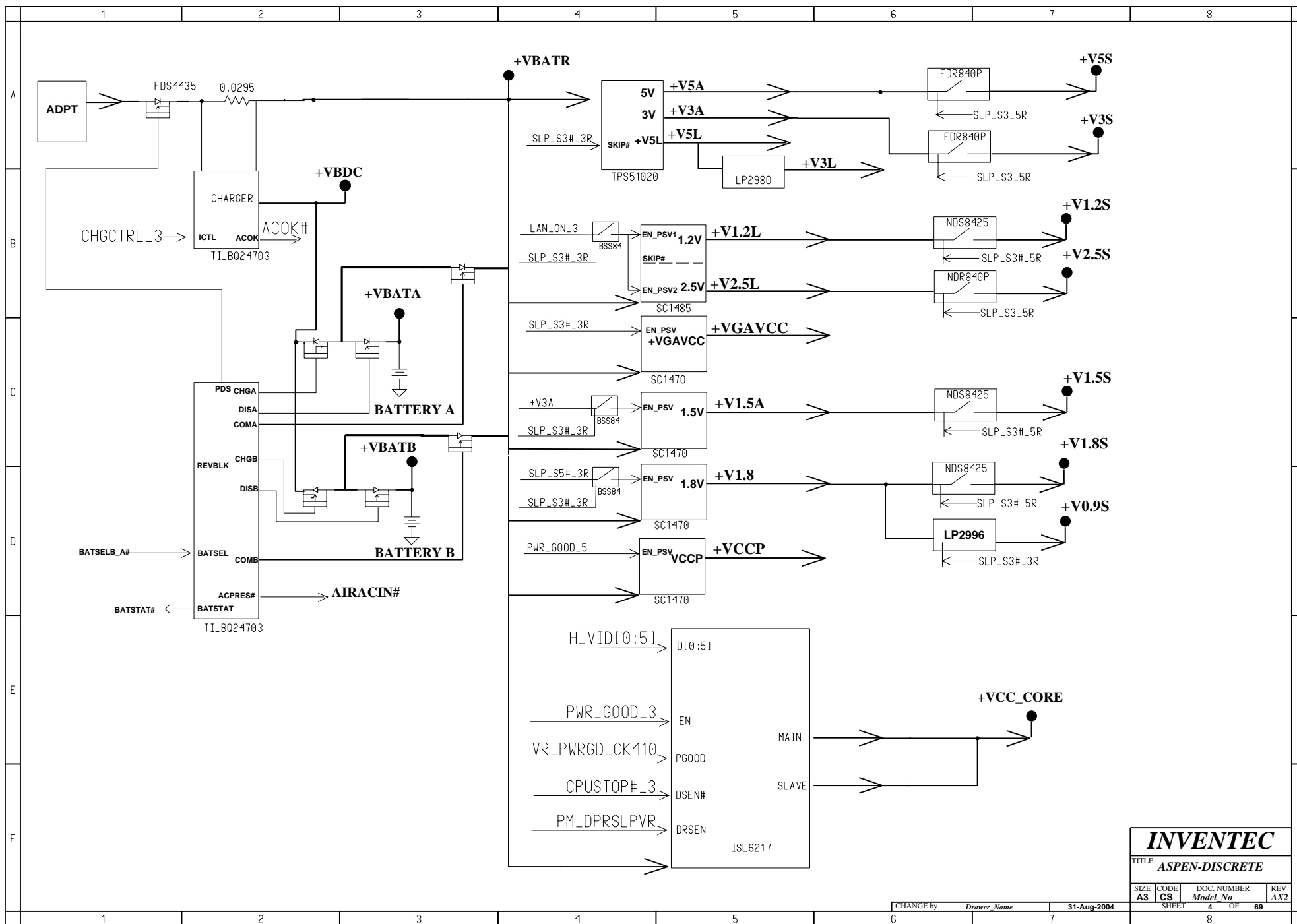
TITLE
ASPEN-DISCRETE

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX2
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CHANGE by *Drawer Name* 31-Aug-2004

SHEET 2 OF 69





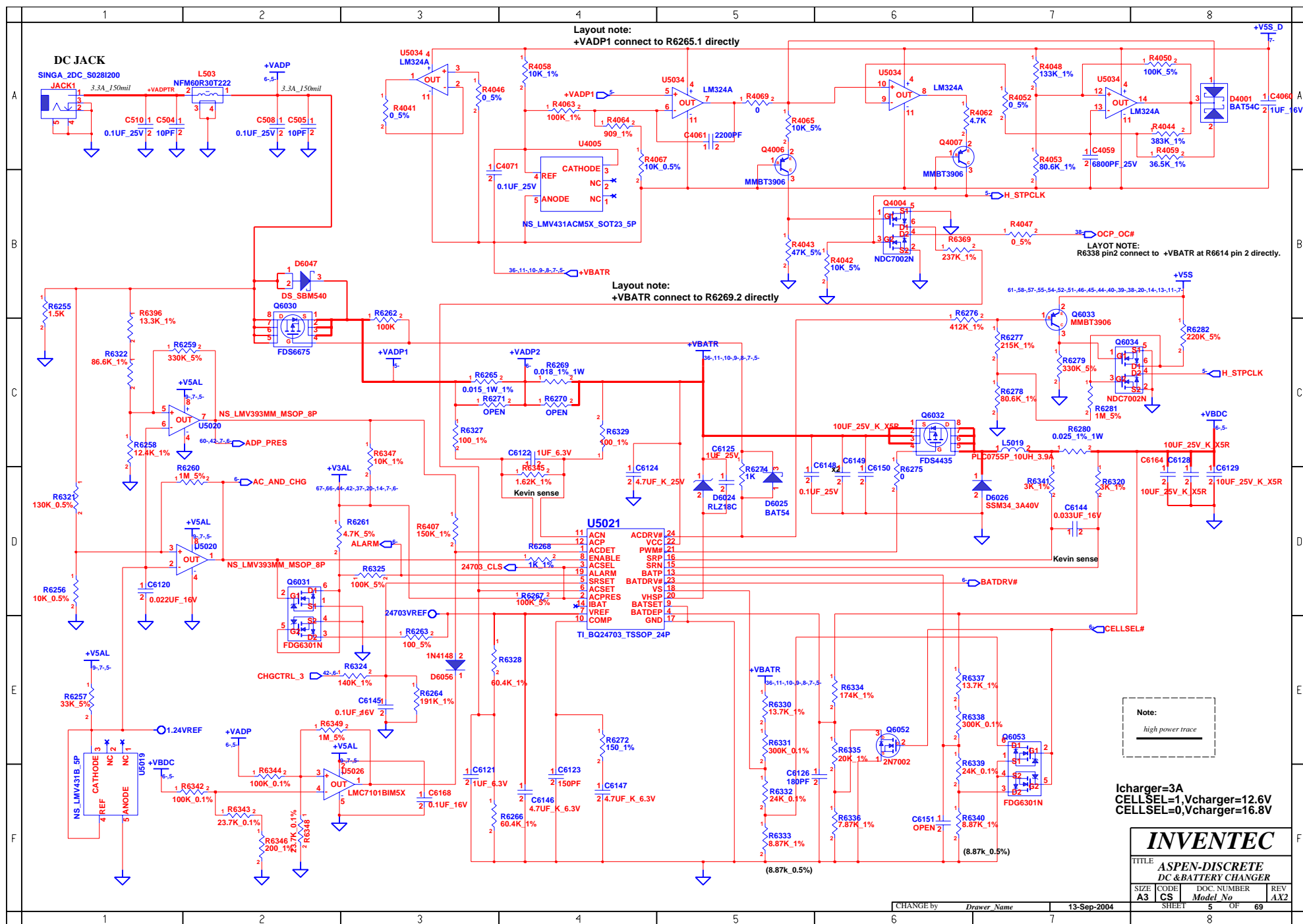
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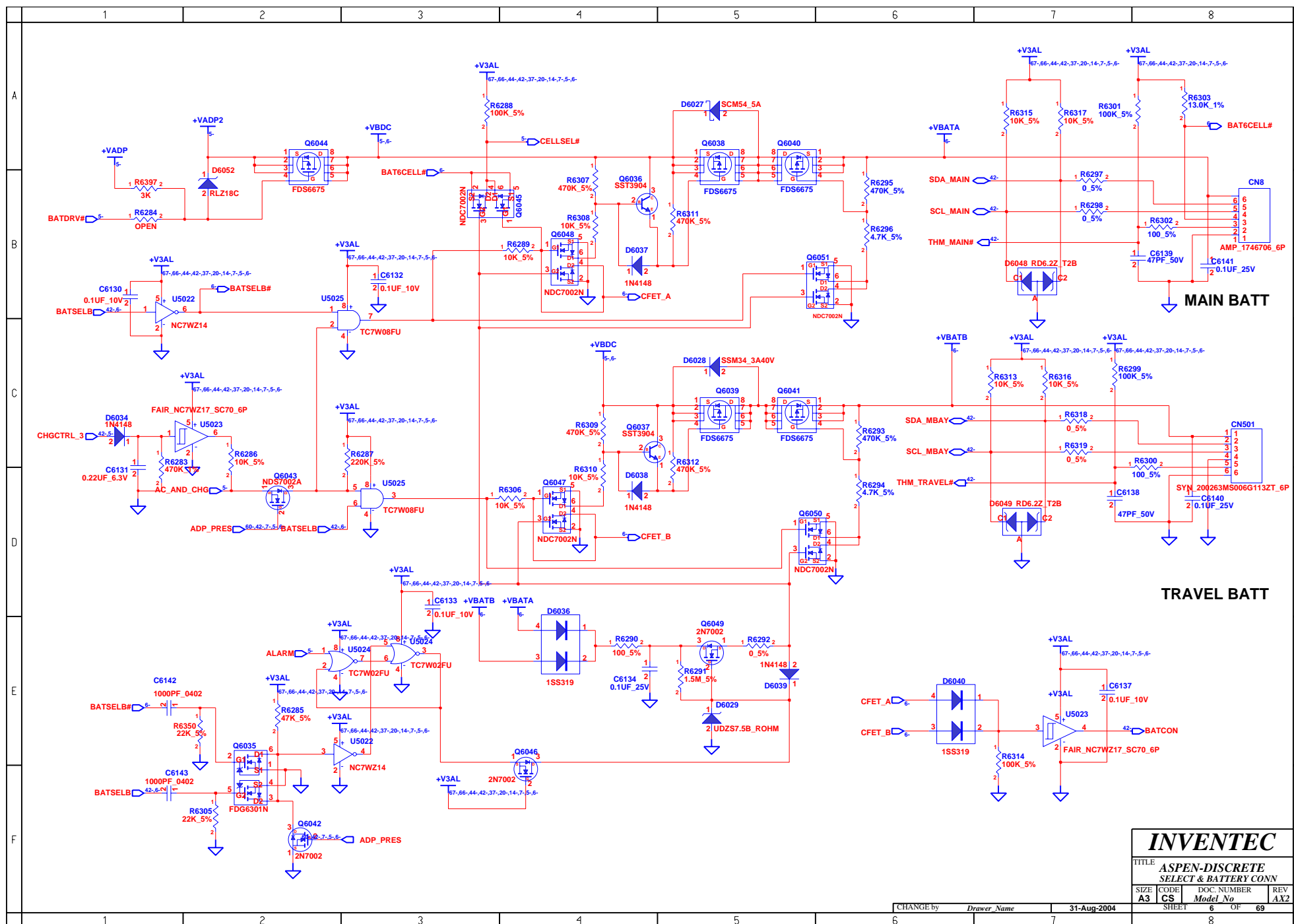
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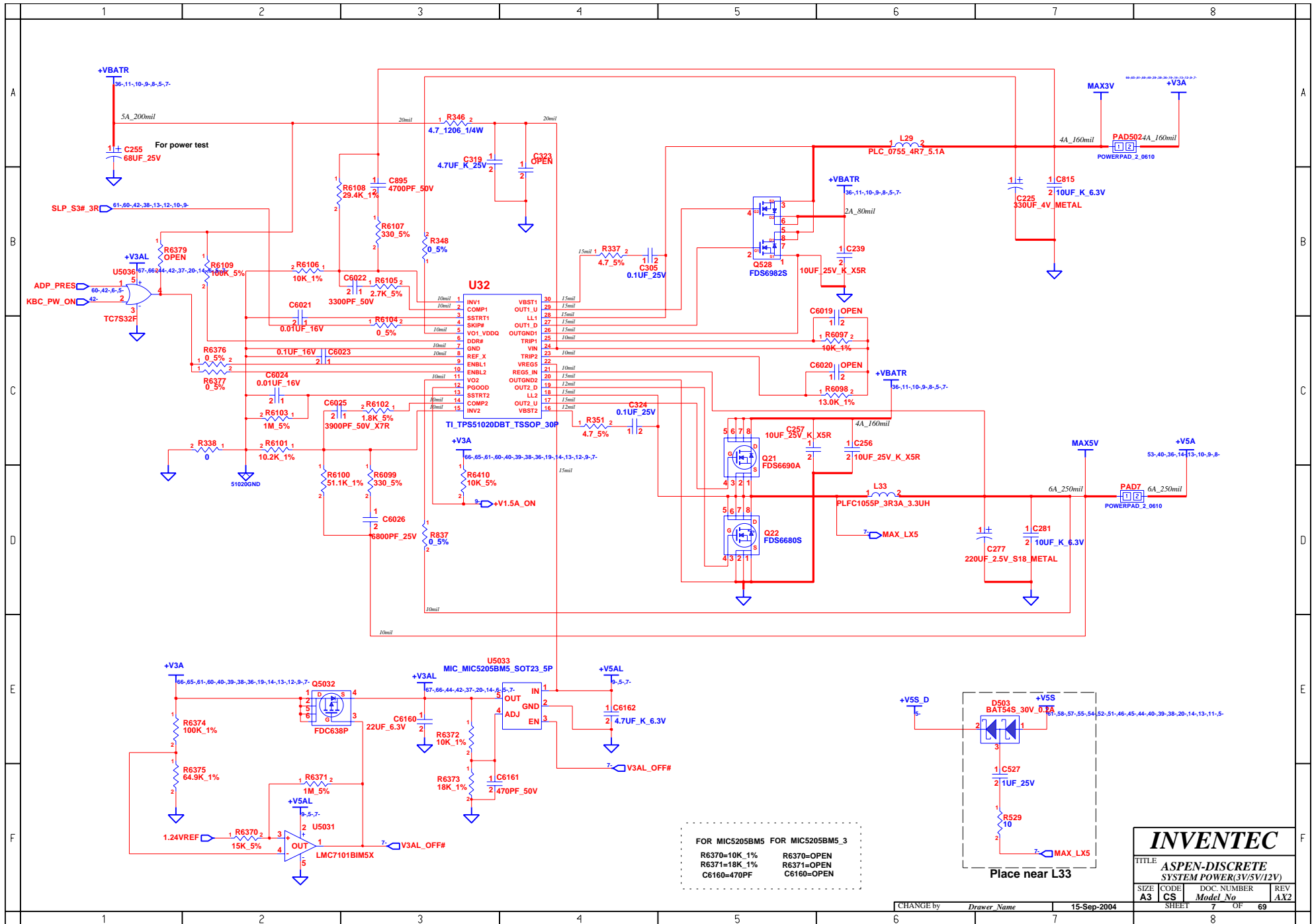
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A3	CS	Model No	AX2

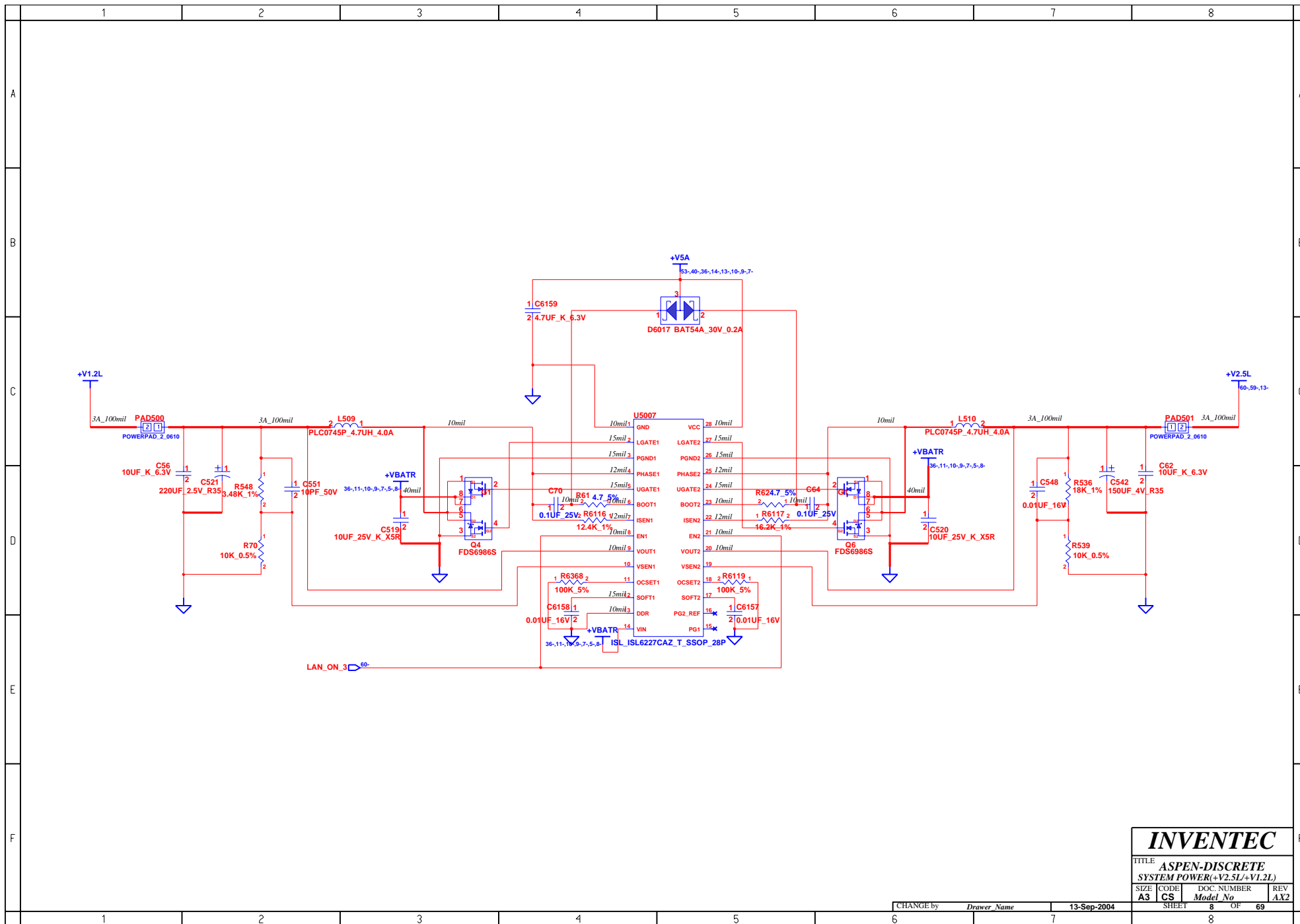
CHANGE by *Drawer Name* 31-Aug-2004

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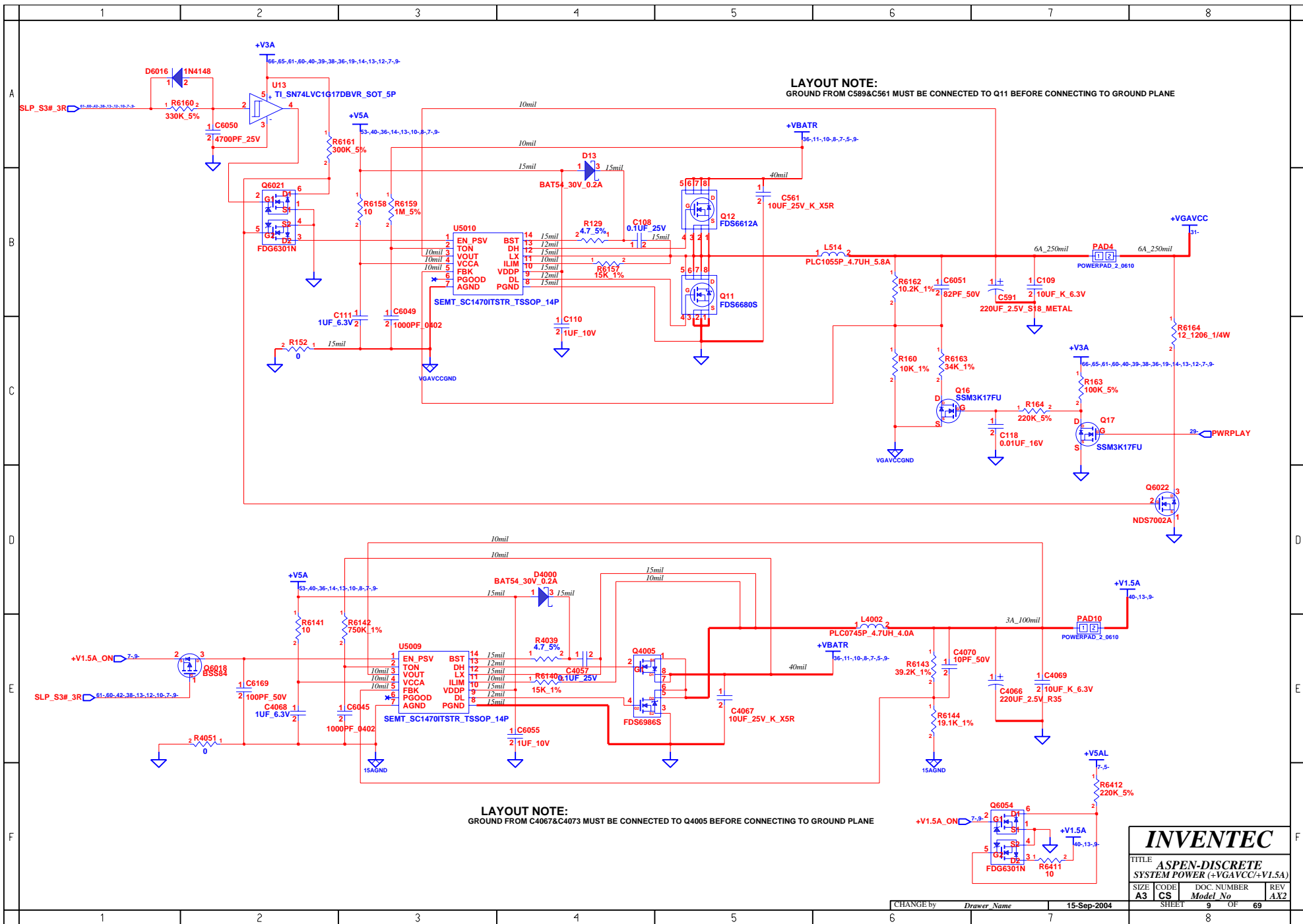


INVENTEC

TITLE			
ASPEN-DISCRETE			
SYSTEM POWER(+V2.5L/+V1.2L)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawer Name* 13-Sep-2004

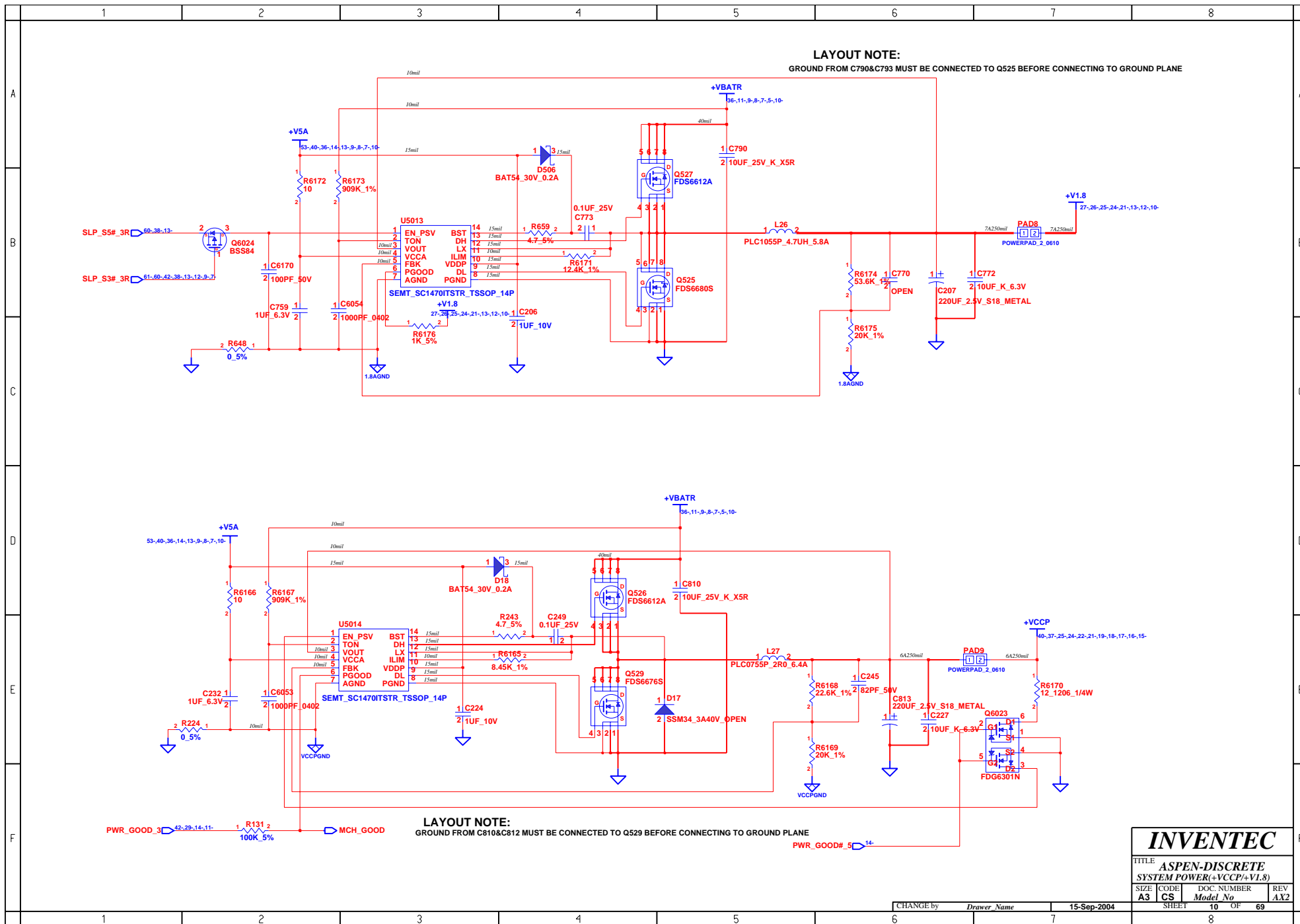
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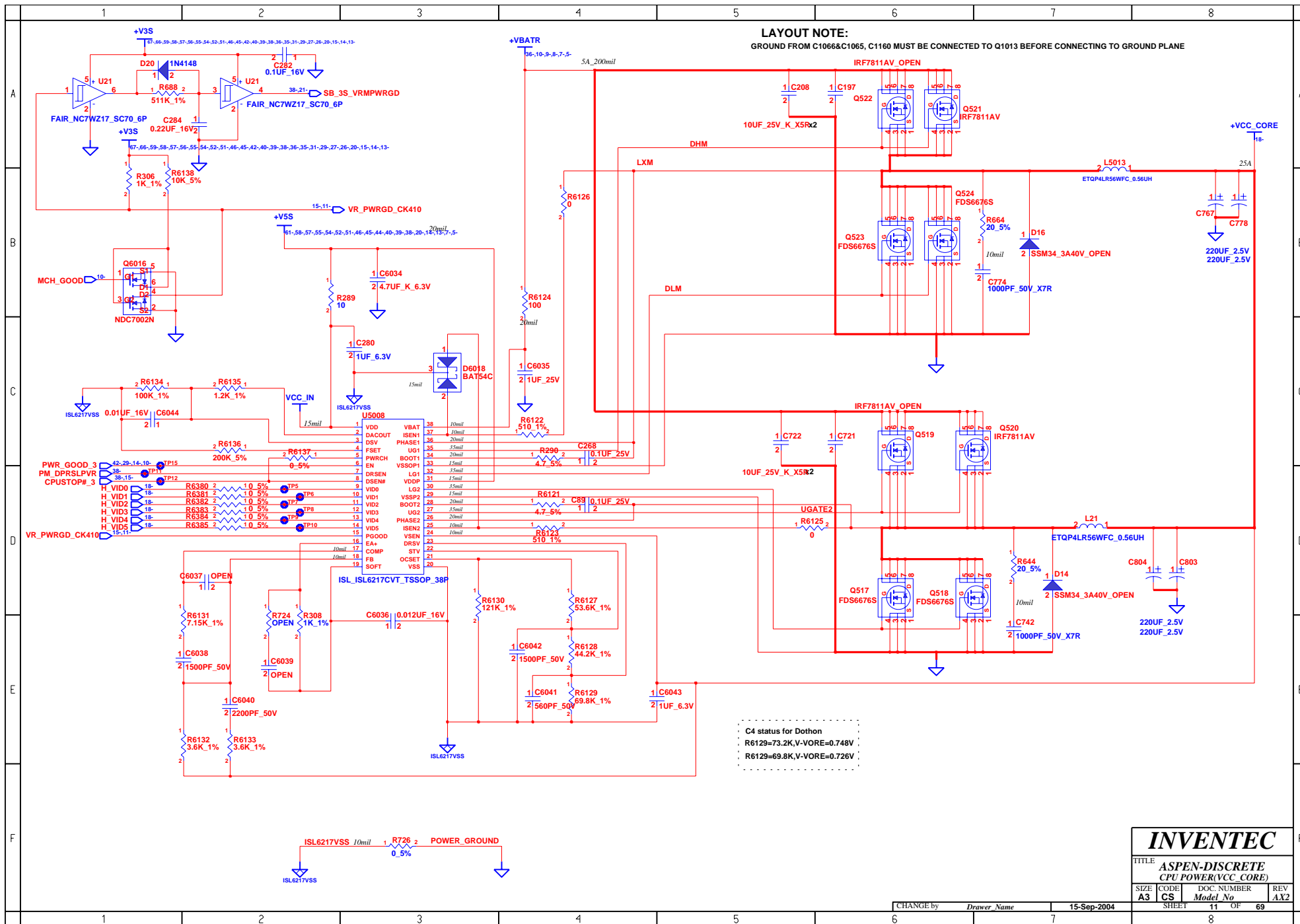


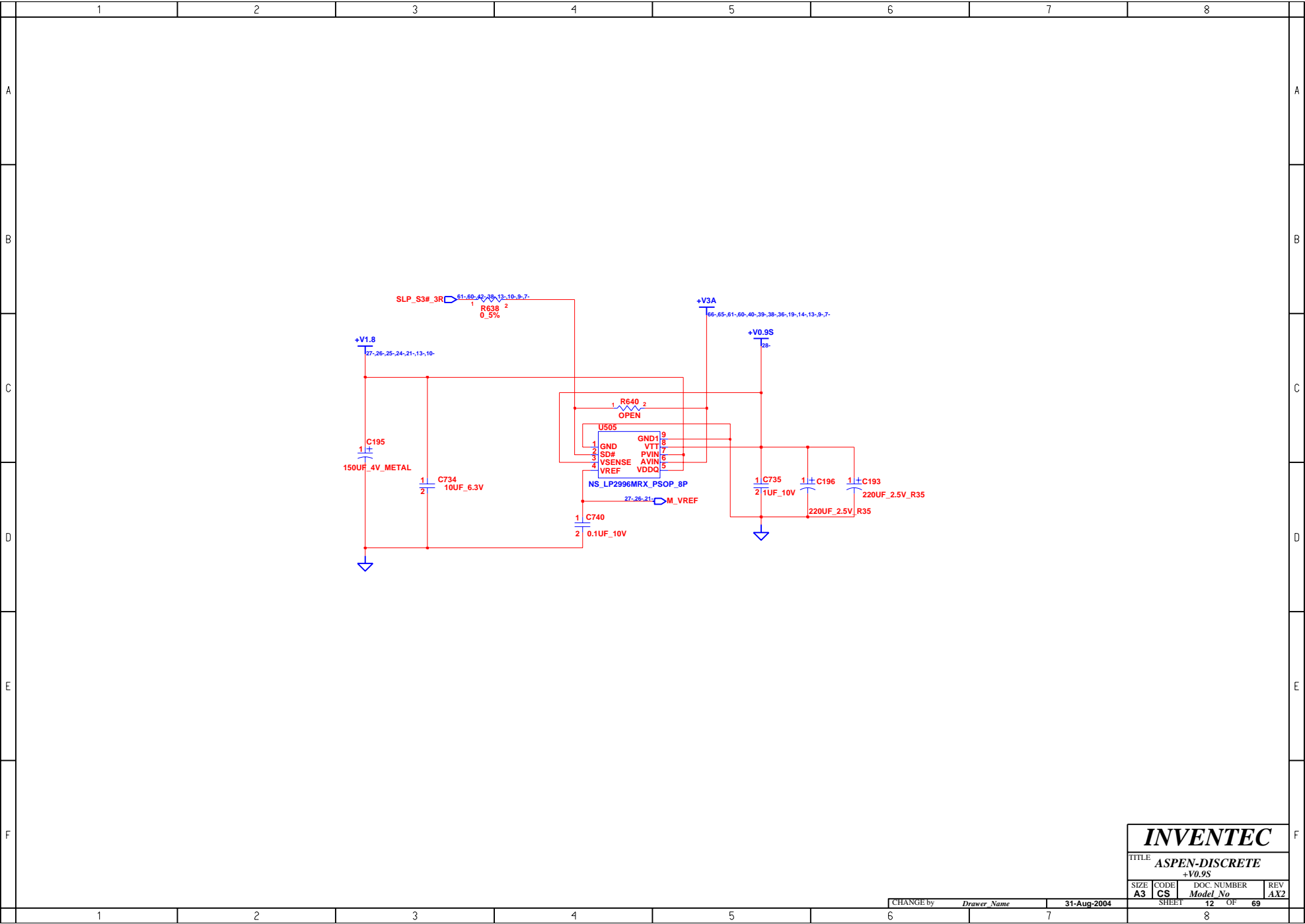
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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2

CHANGE by: Drawer Name 15-Sep-2004

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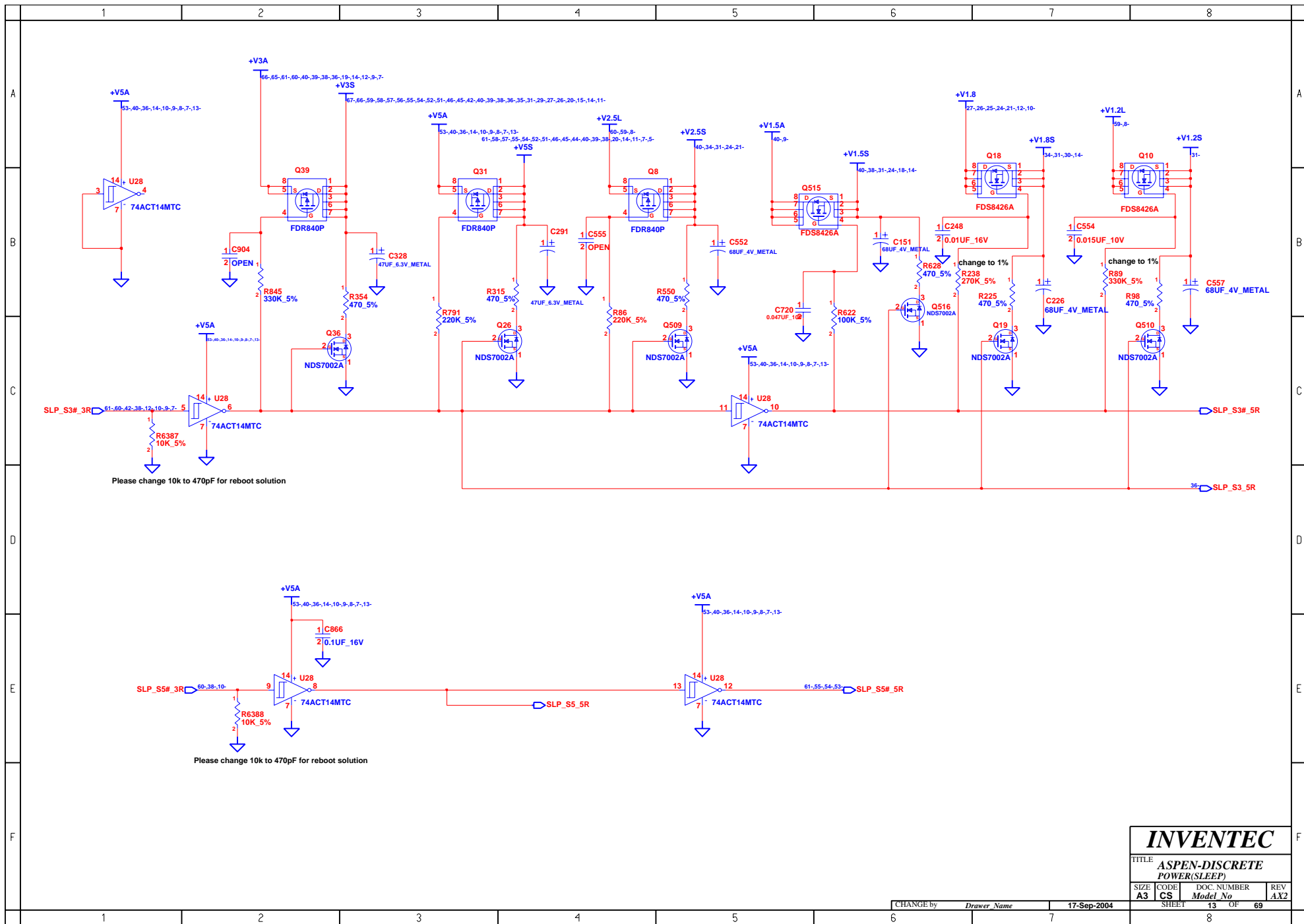
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ASPEN-DISCRETE

SIZE CODE DOC. NUMBER REV
A3 CS Model No AX2

CHANGE by *Drawer Name* 31-Aug-2004

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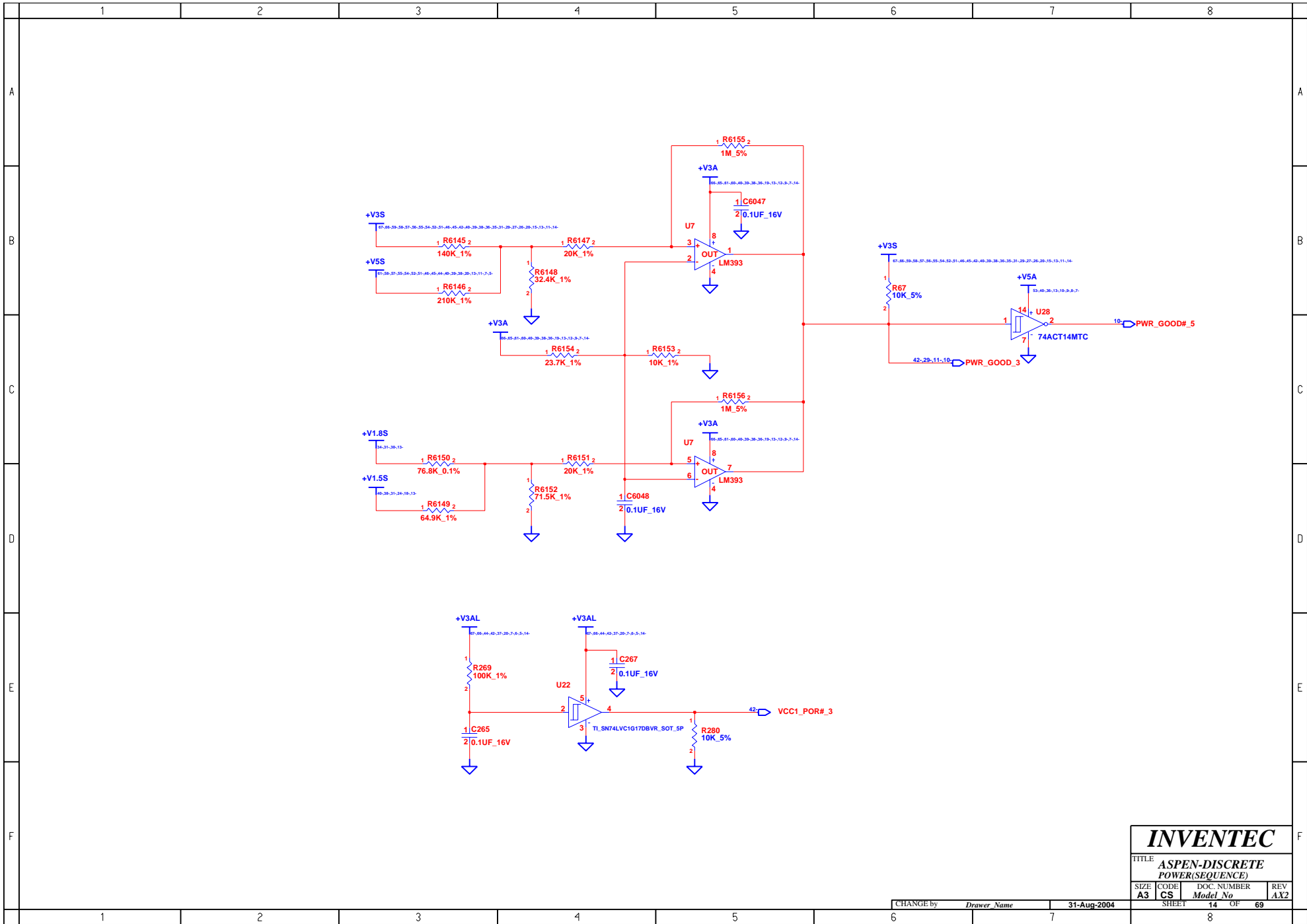


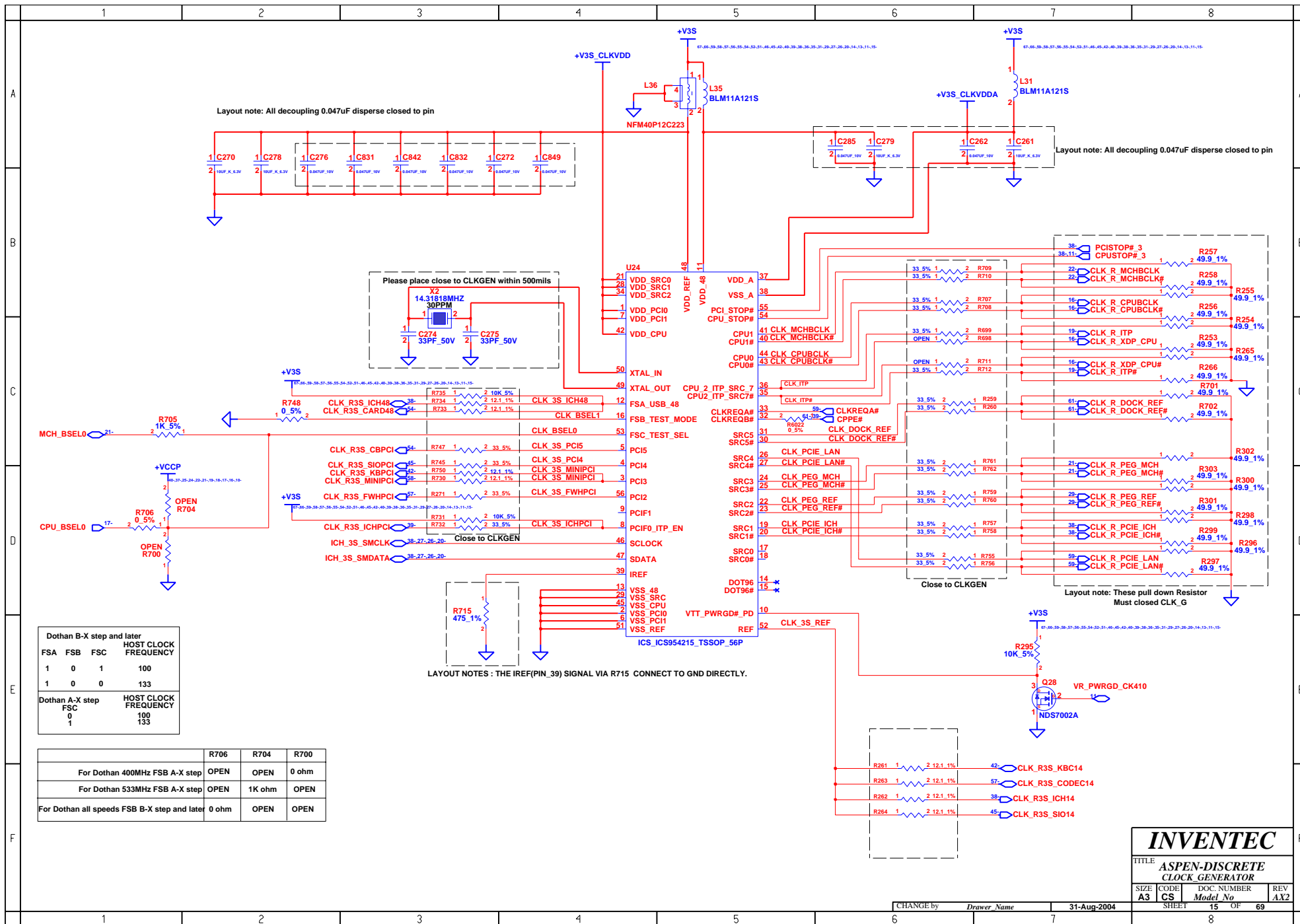
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TITLE
**ASPEN-DISCRETE
POWER(SLEEP)**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by	Drawer Name	17-Sep-2004
SHEET	13	OF 69

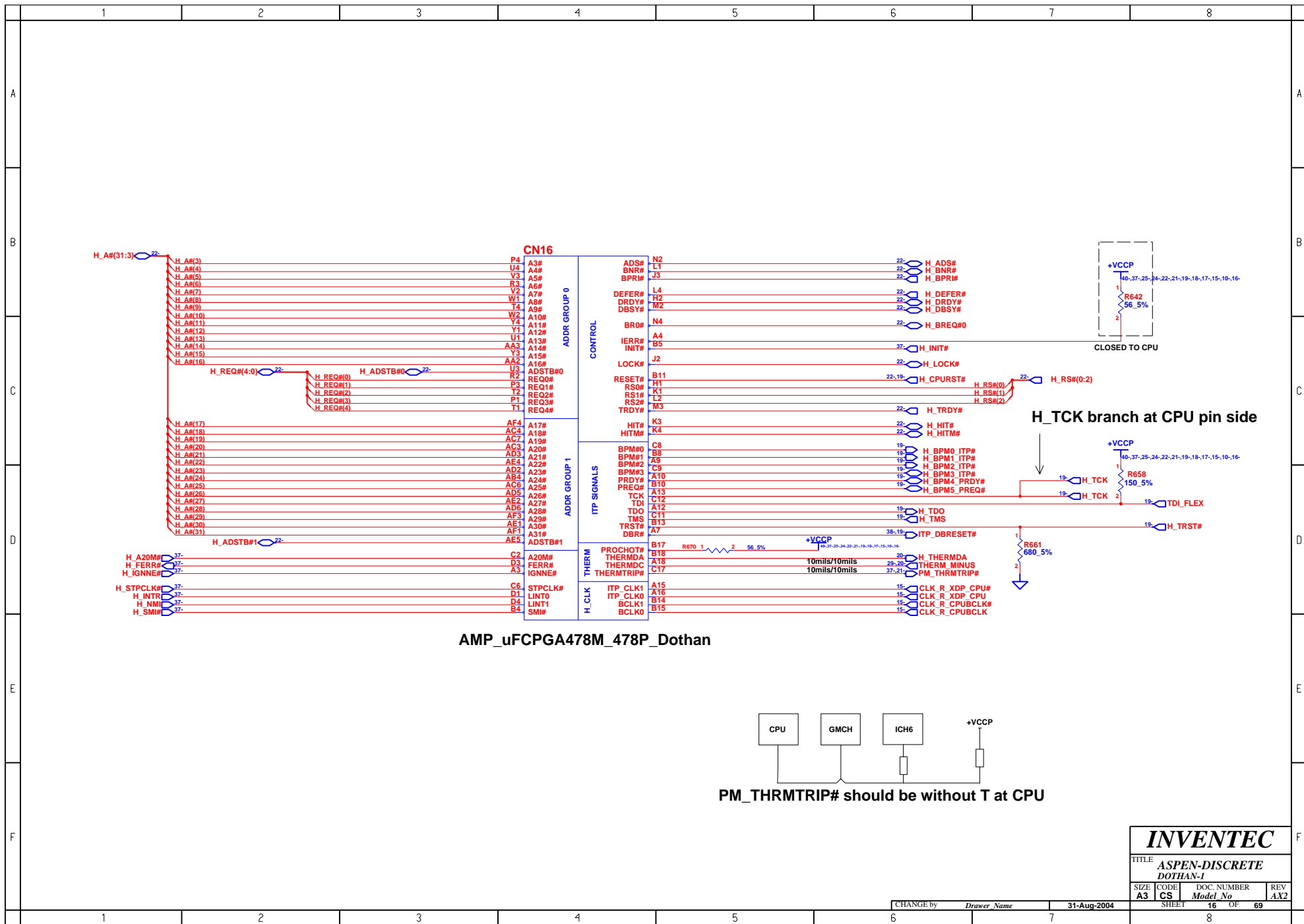


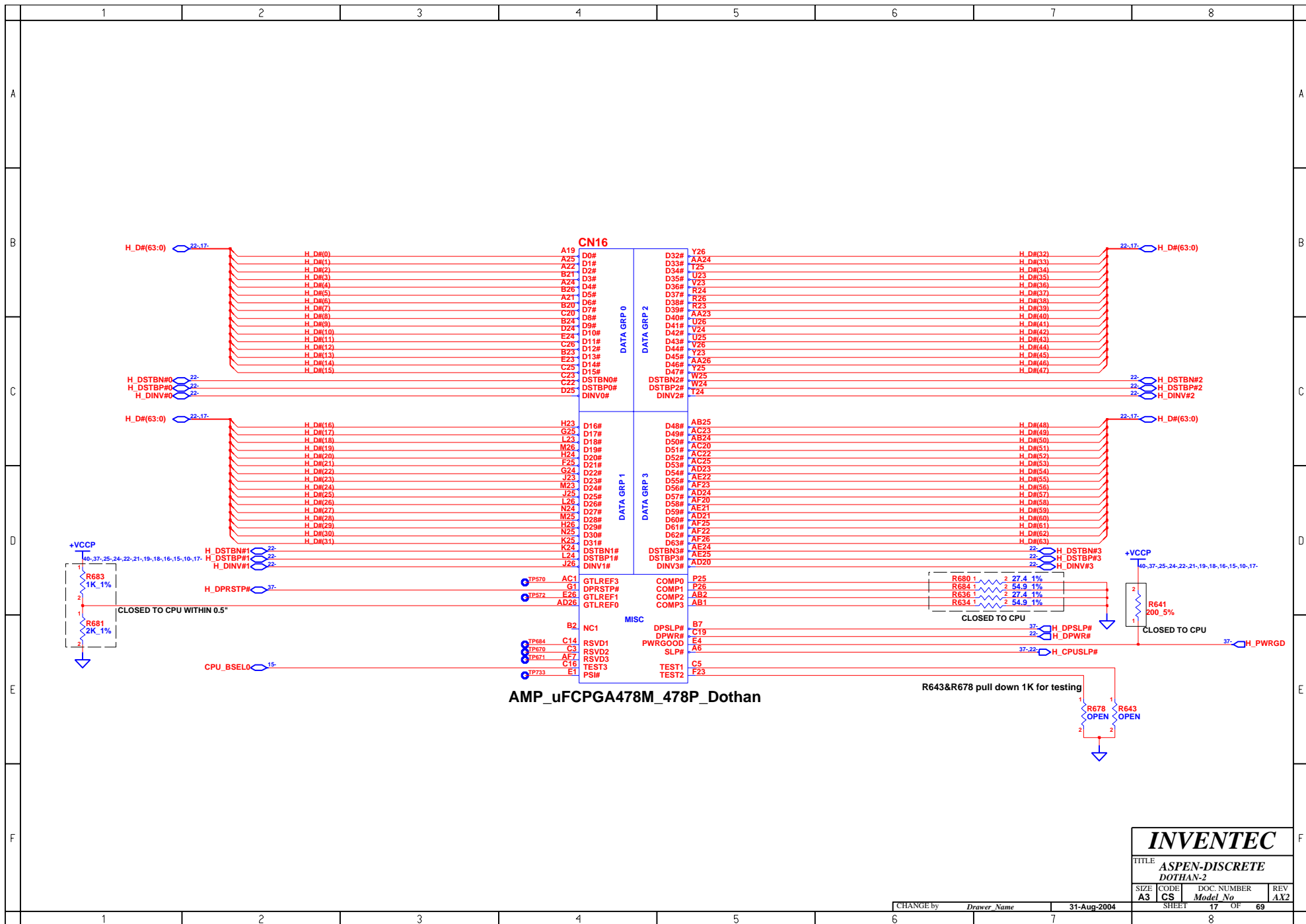


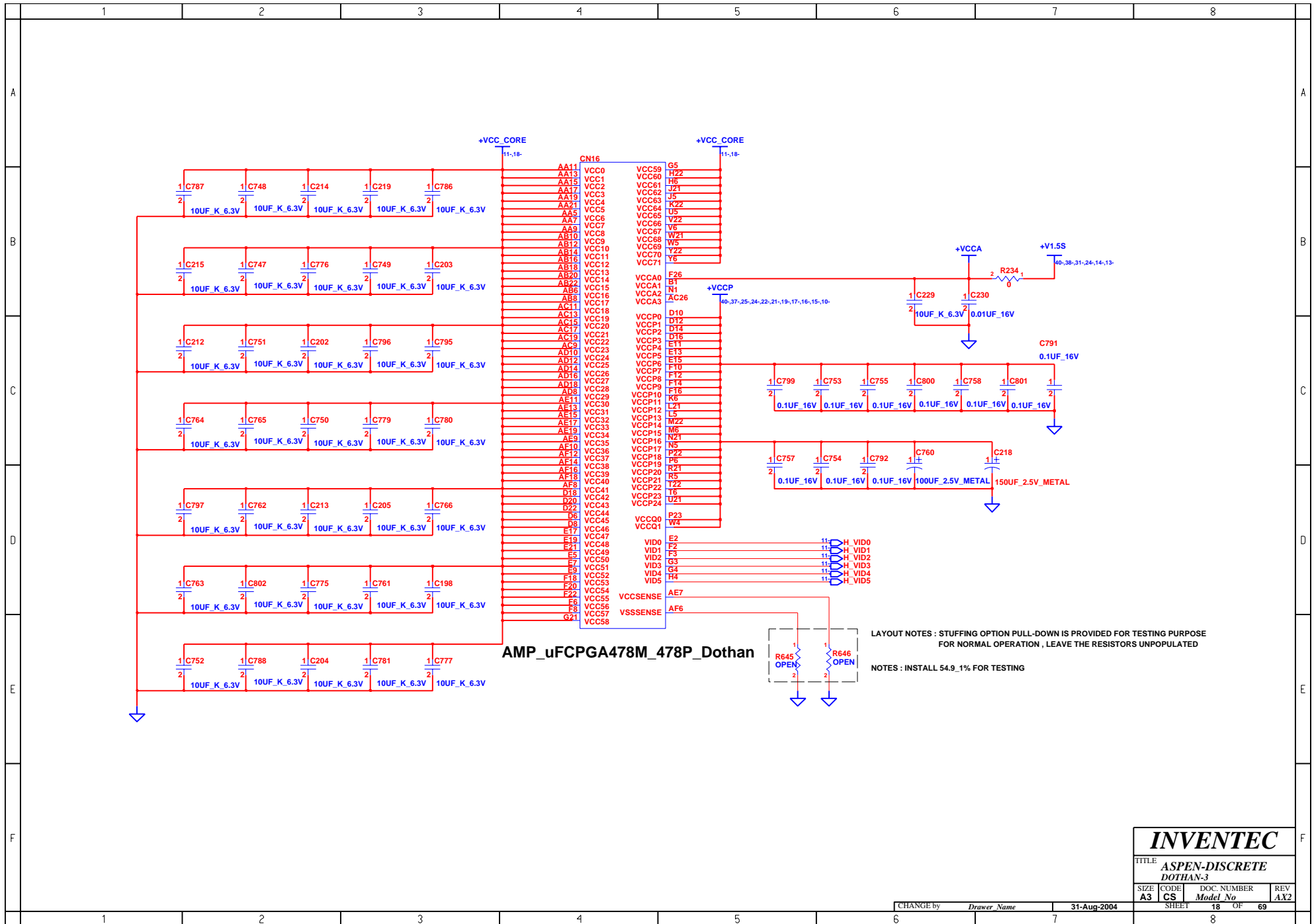
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CLOCK GENERATOR**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2







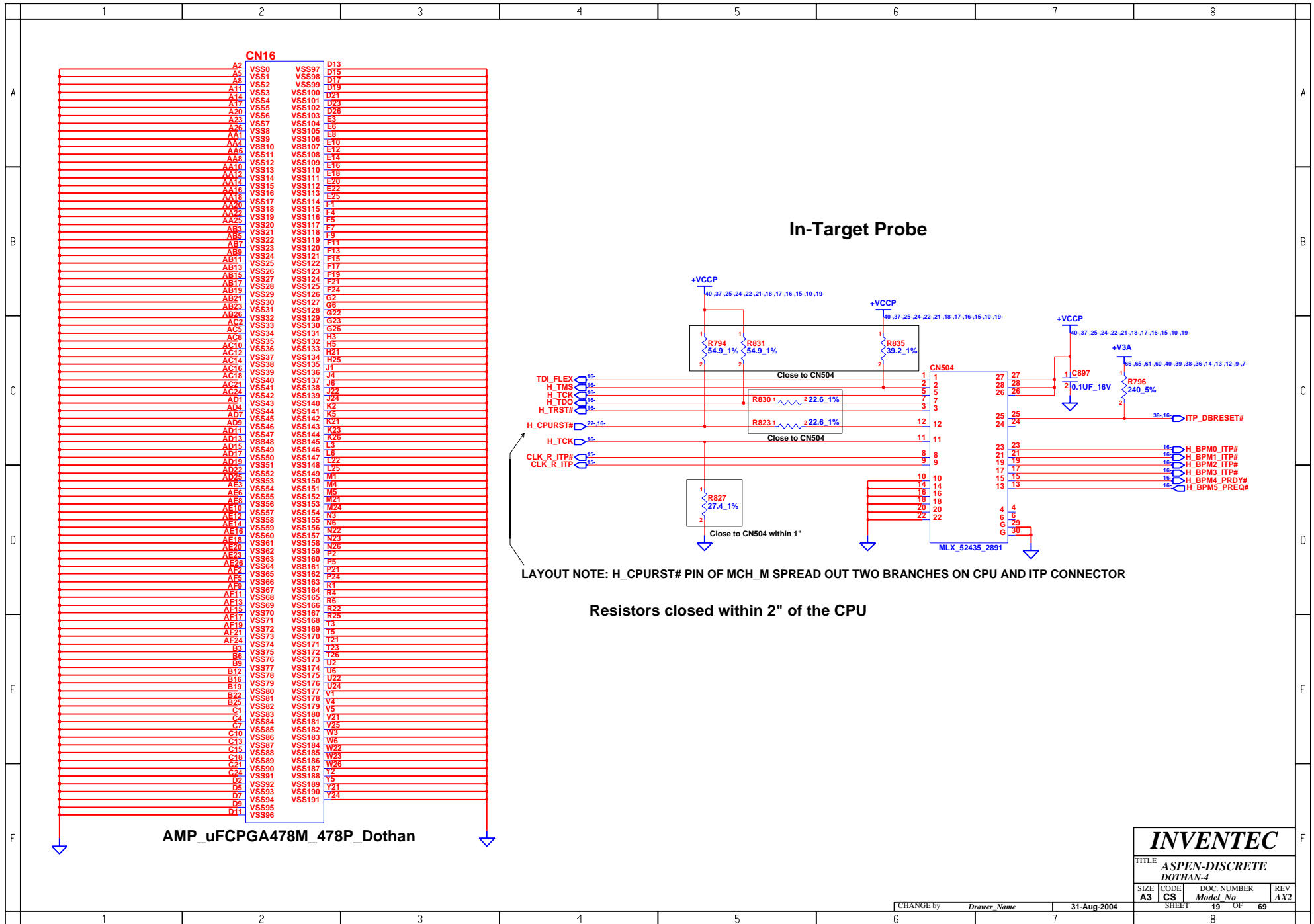
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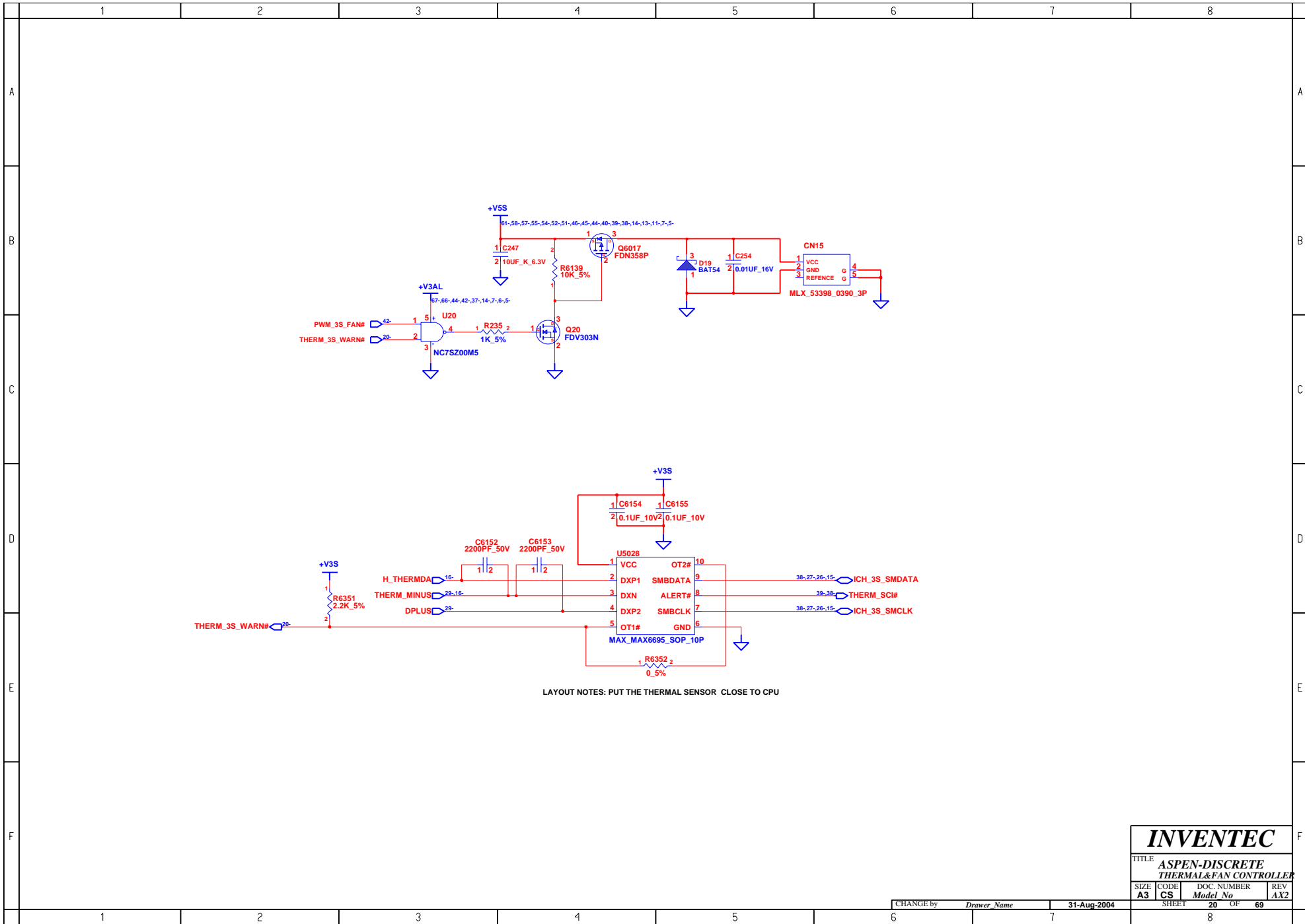
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**ASPEN-DISCRETE
DOTHAN-3**

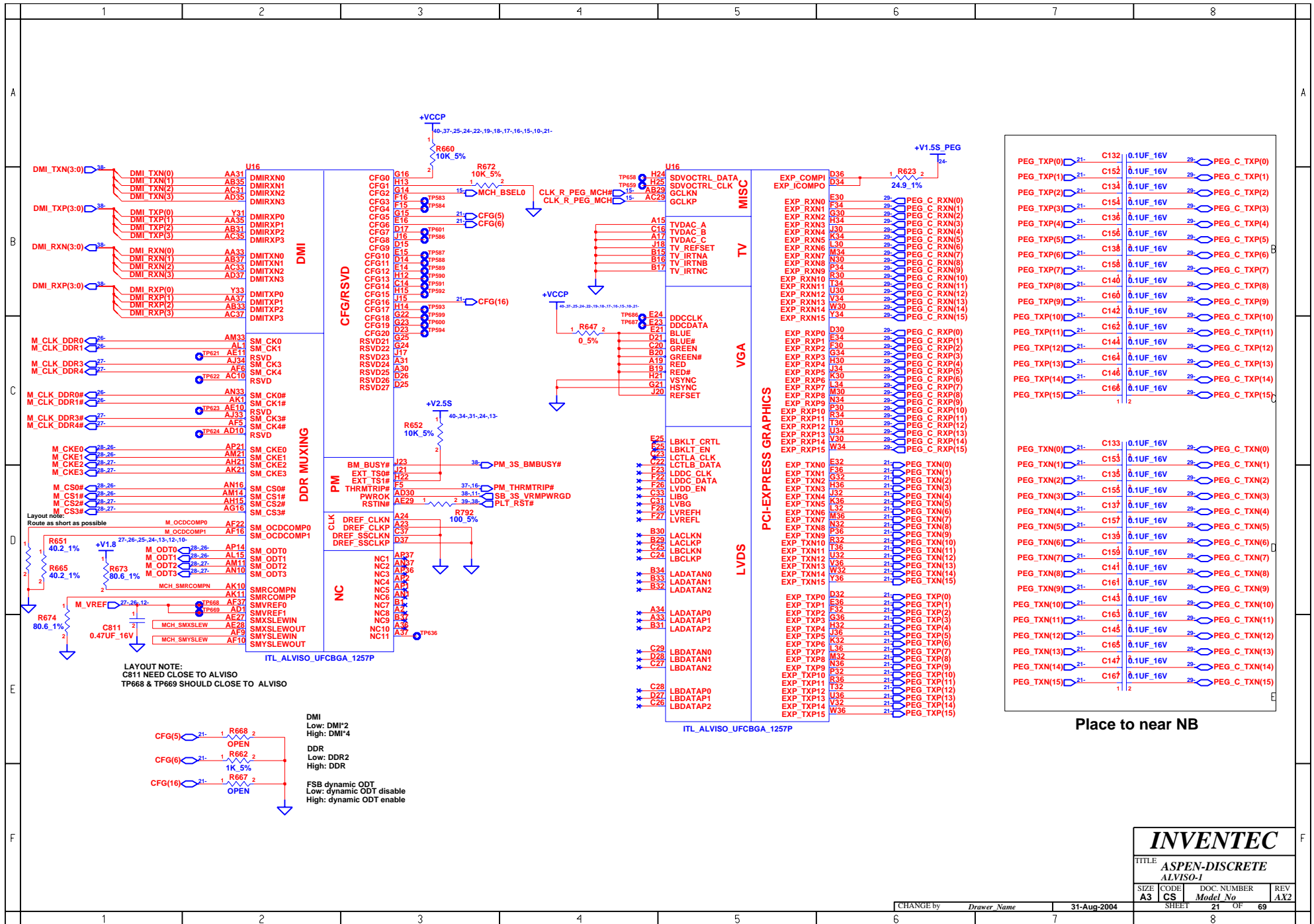
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A3	CS	Model No	AX2

CHANGE by *Drawer Name* 31-Aug-2004

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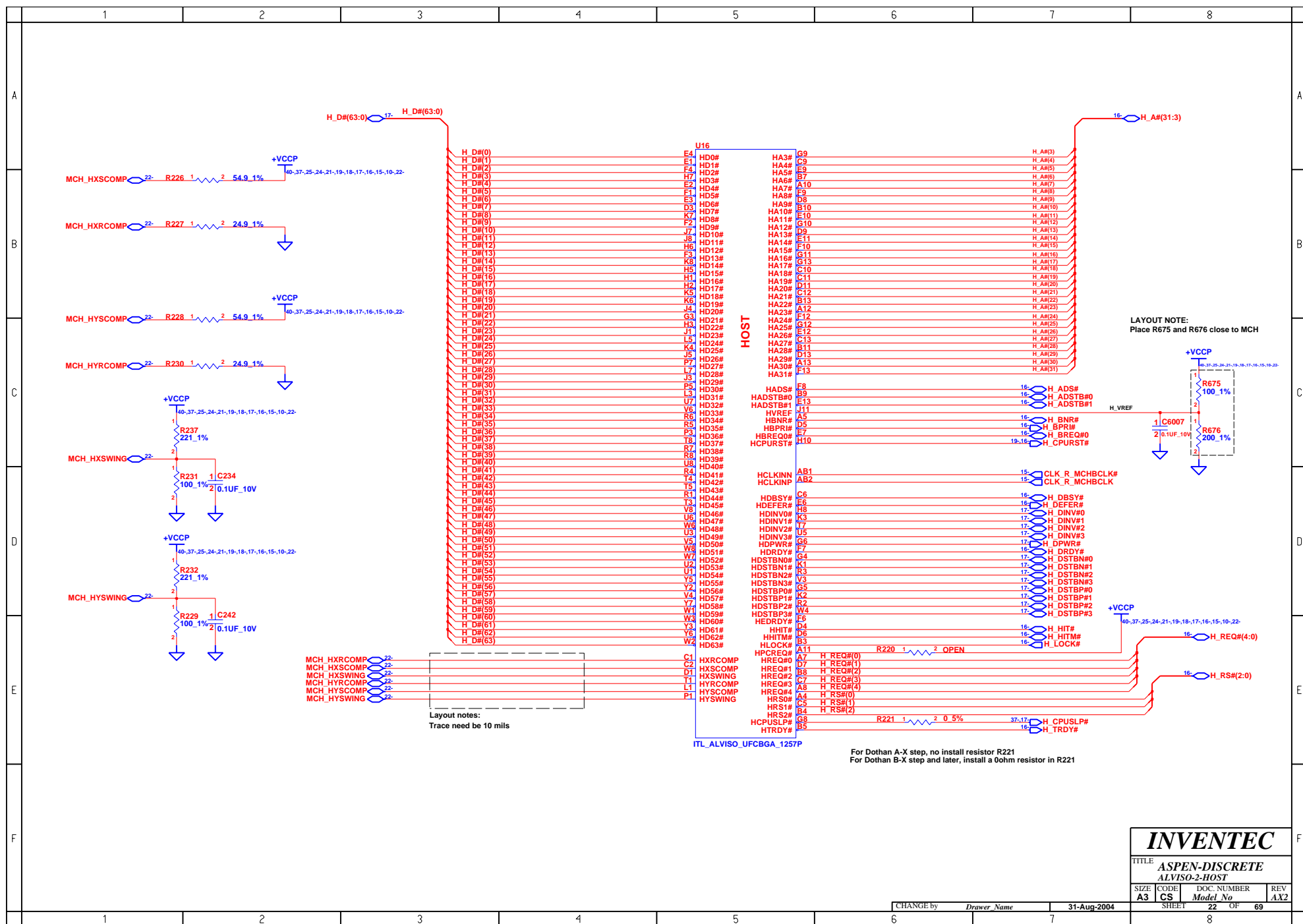
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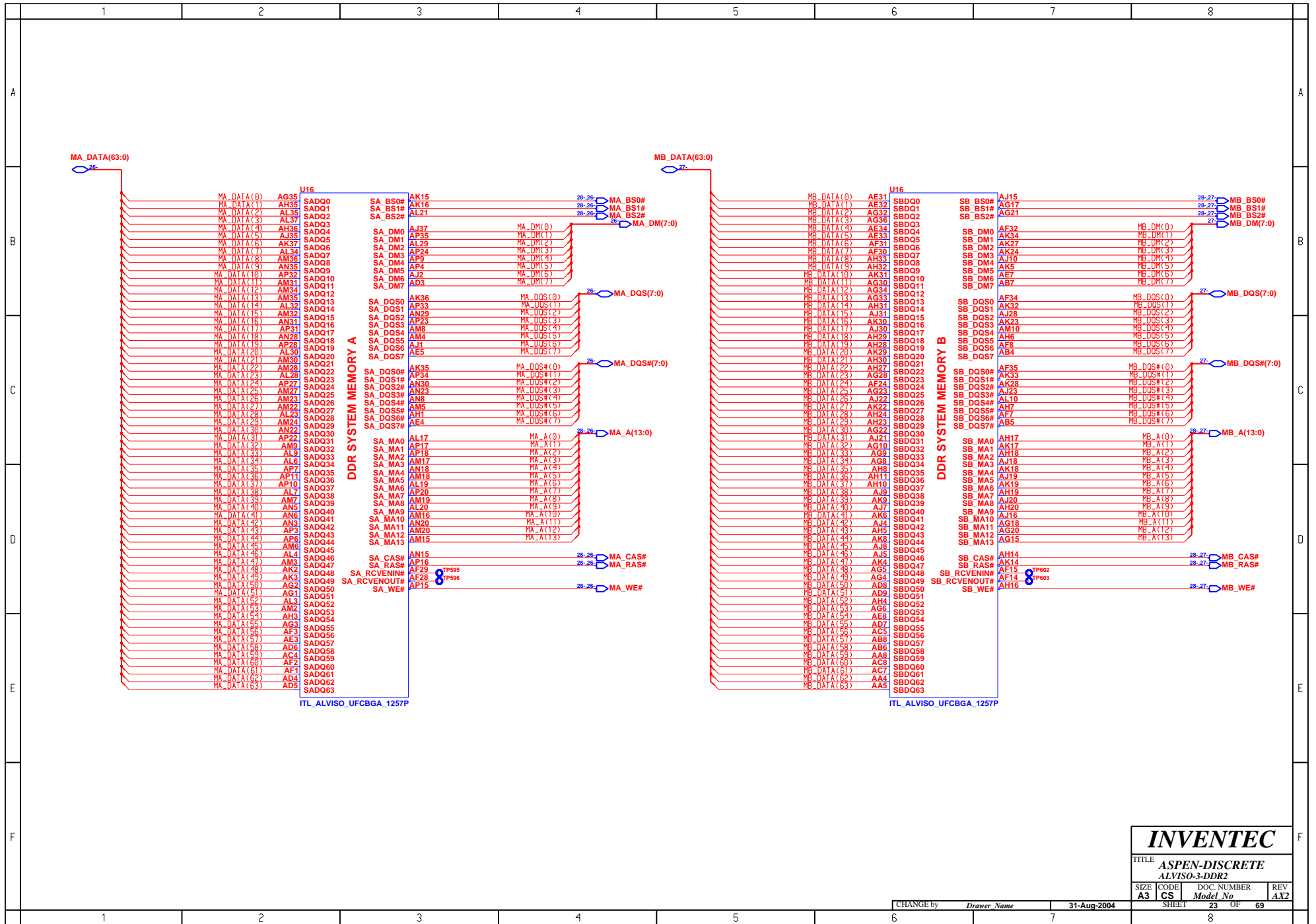
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 ASPEN-DISCRETE
 ALVISO-1

SIZE CODE DOC. NUMBER REV
 A3 CS Model No AX2

CHANGE by Drawer Name 31-Aug-2004

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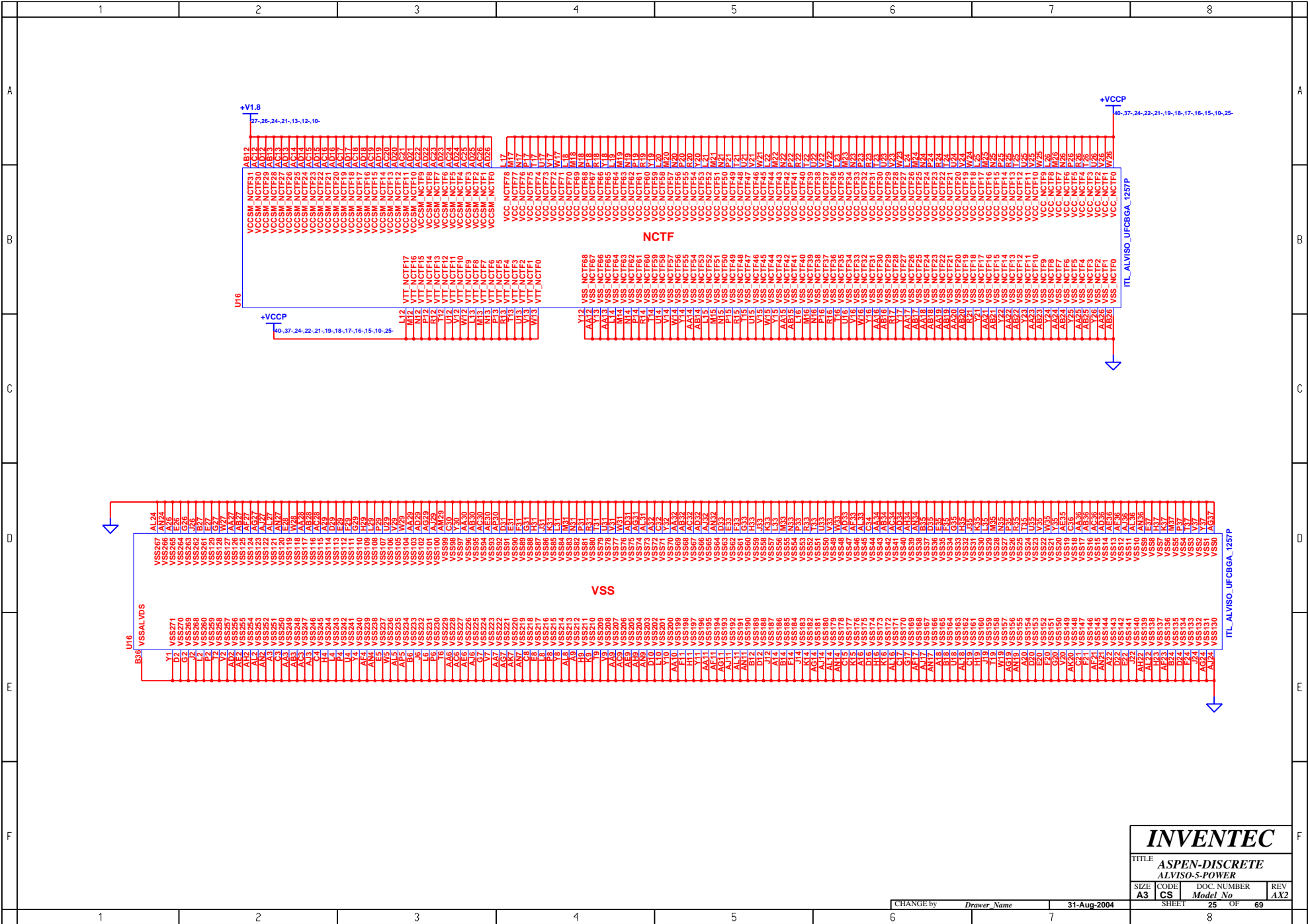
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TITLE
ASPEN-DISCRETE
ALVISO-3-DDR2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by Drawer Name 31-Aug-2004

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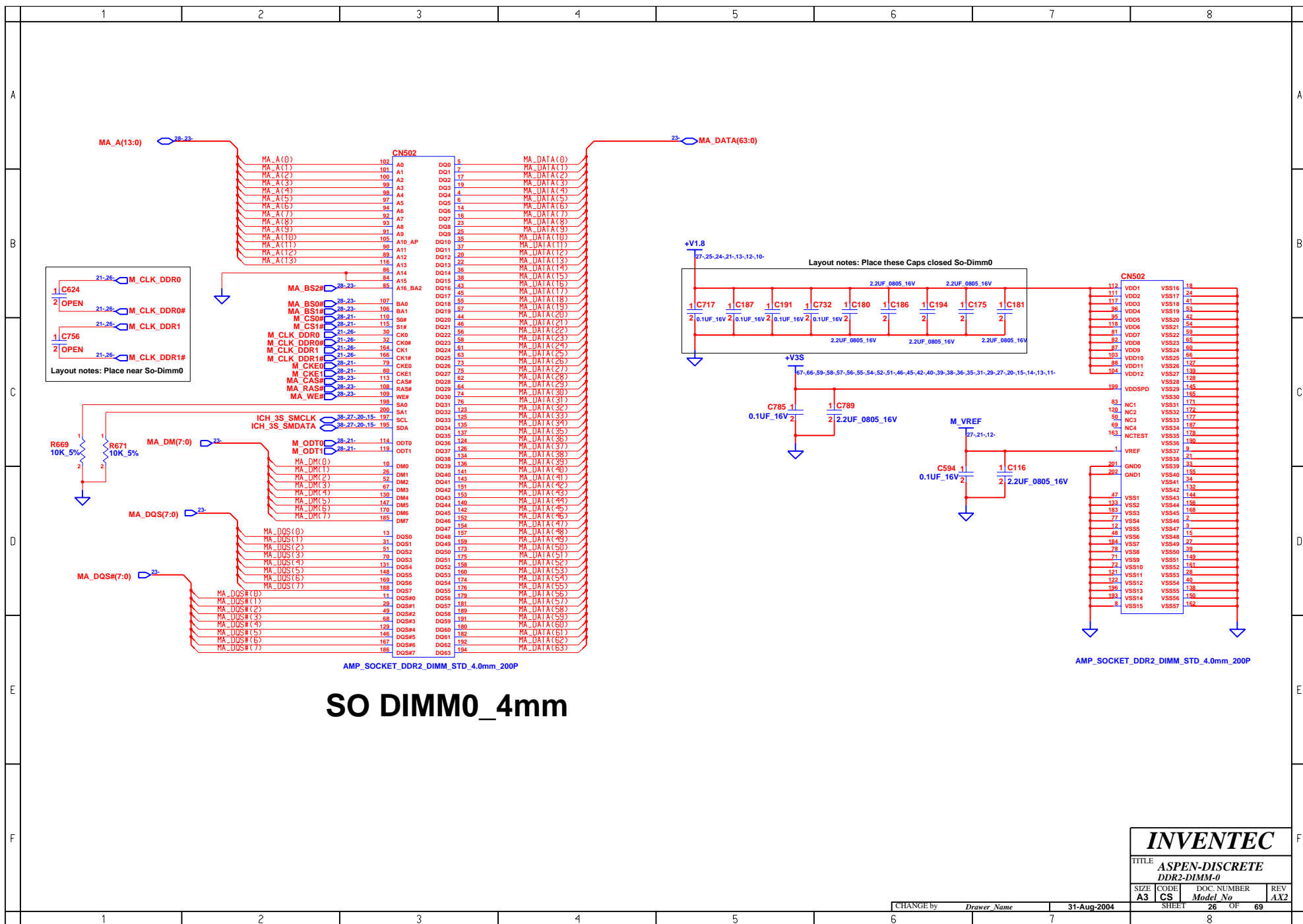
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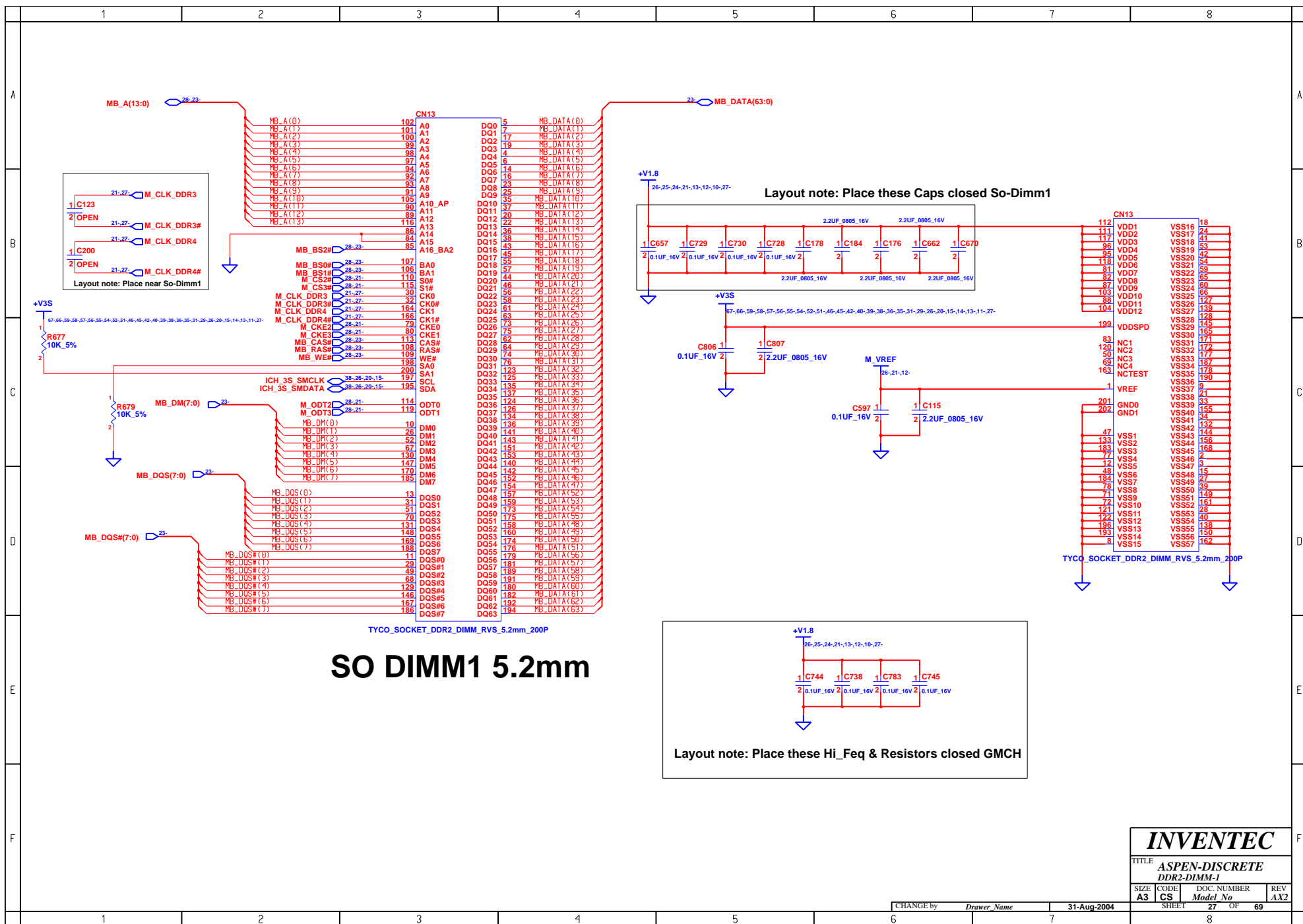
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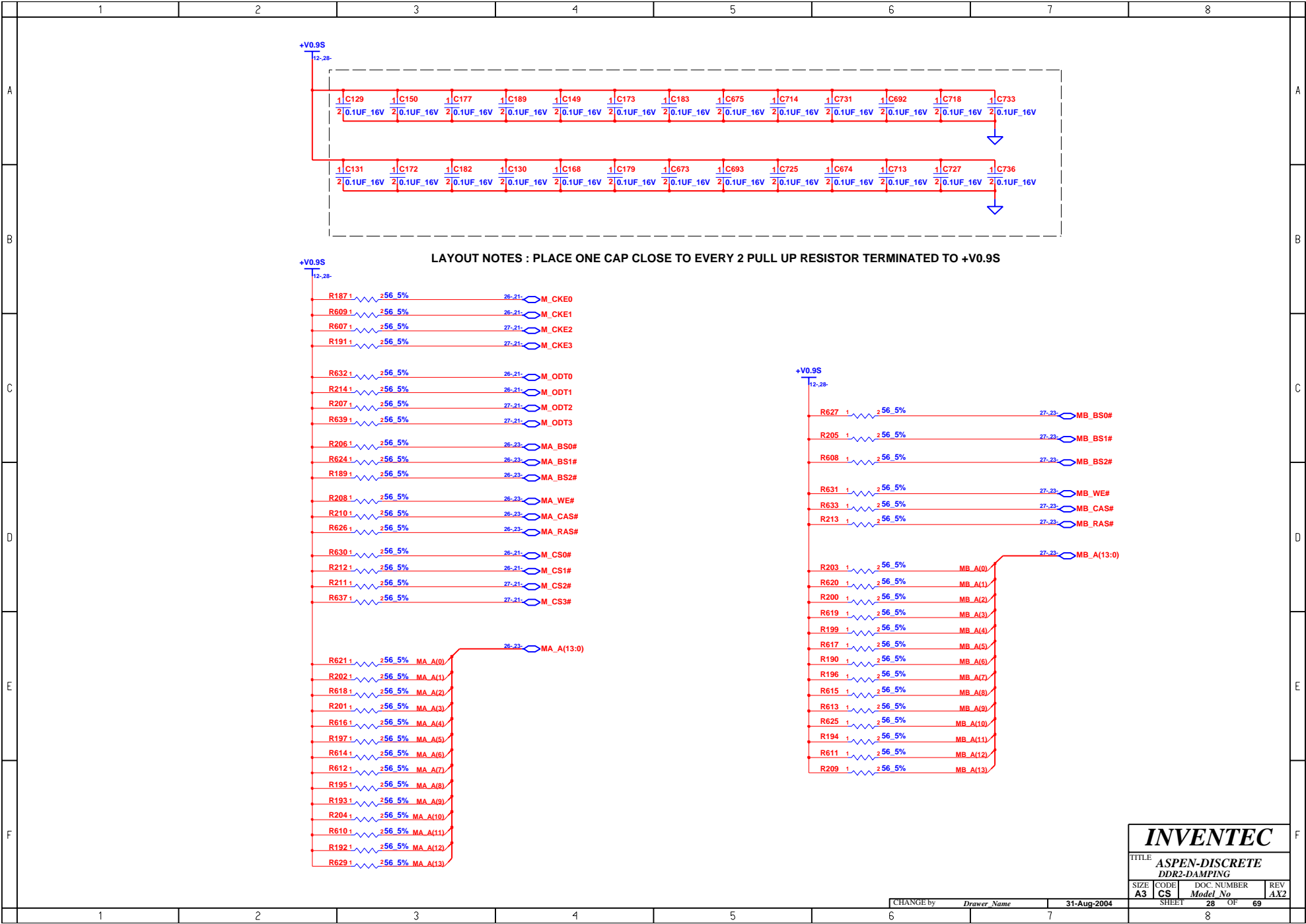
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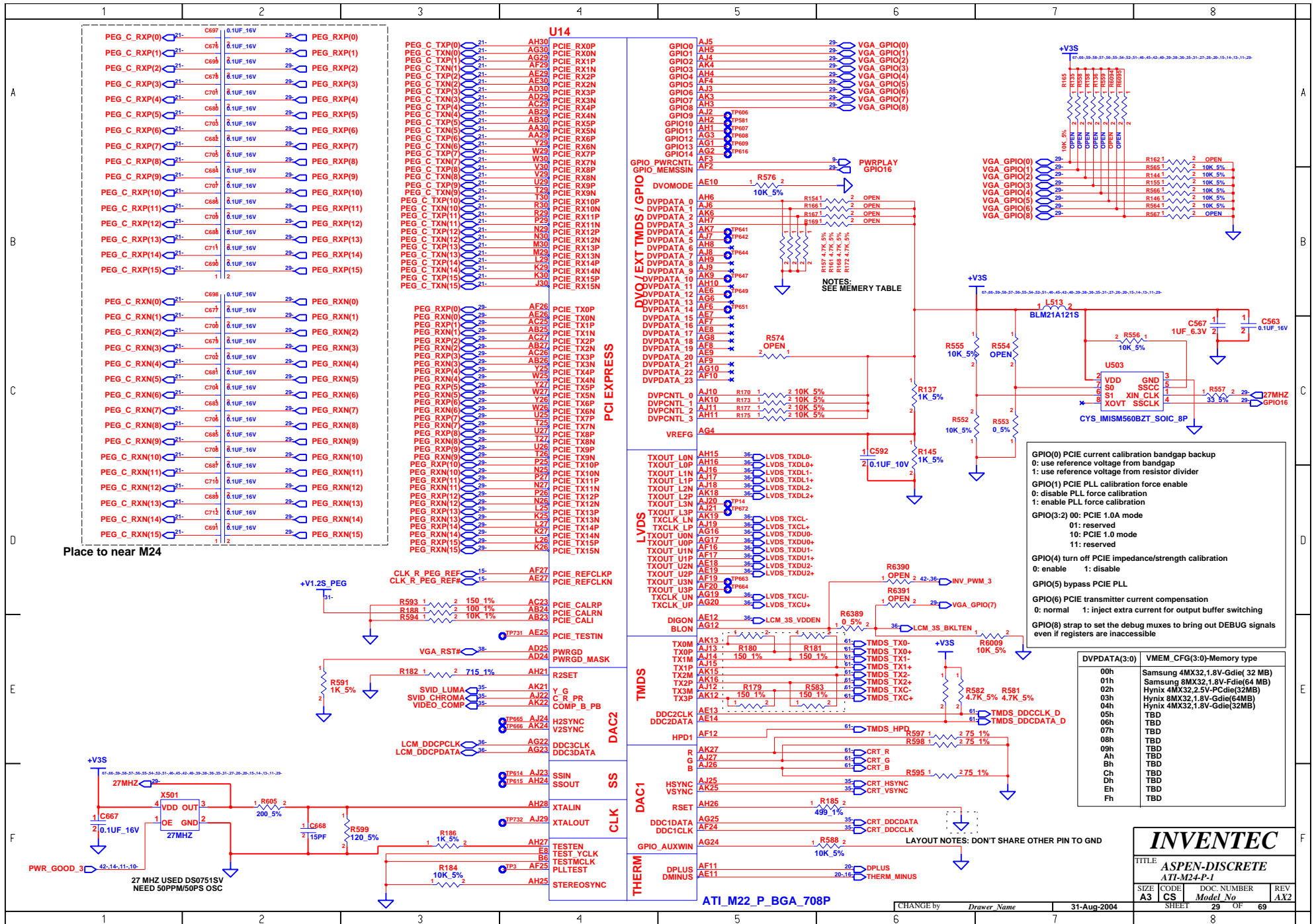
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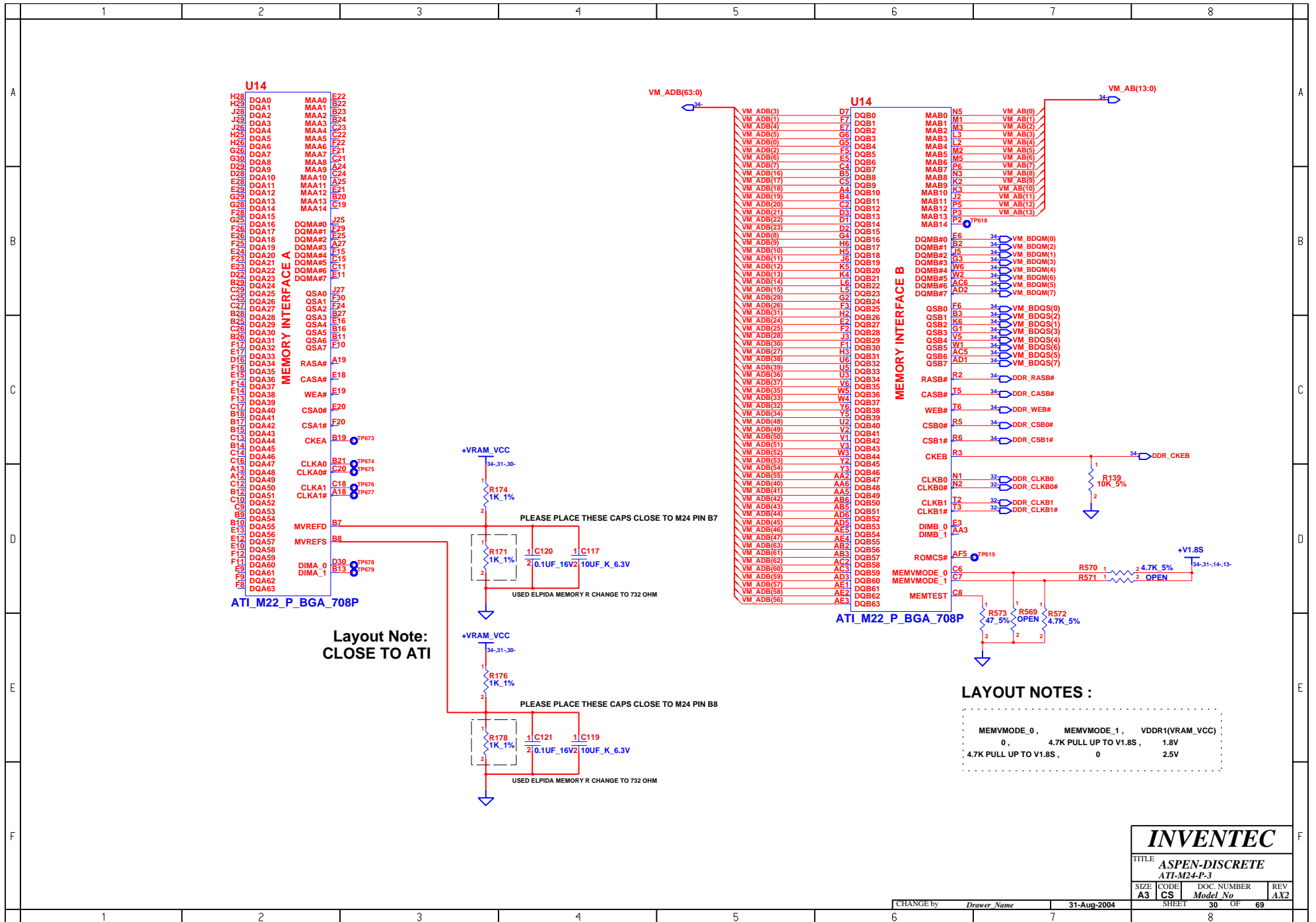
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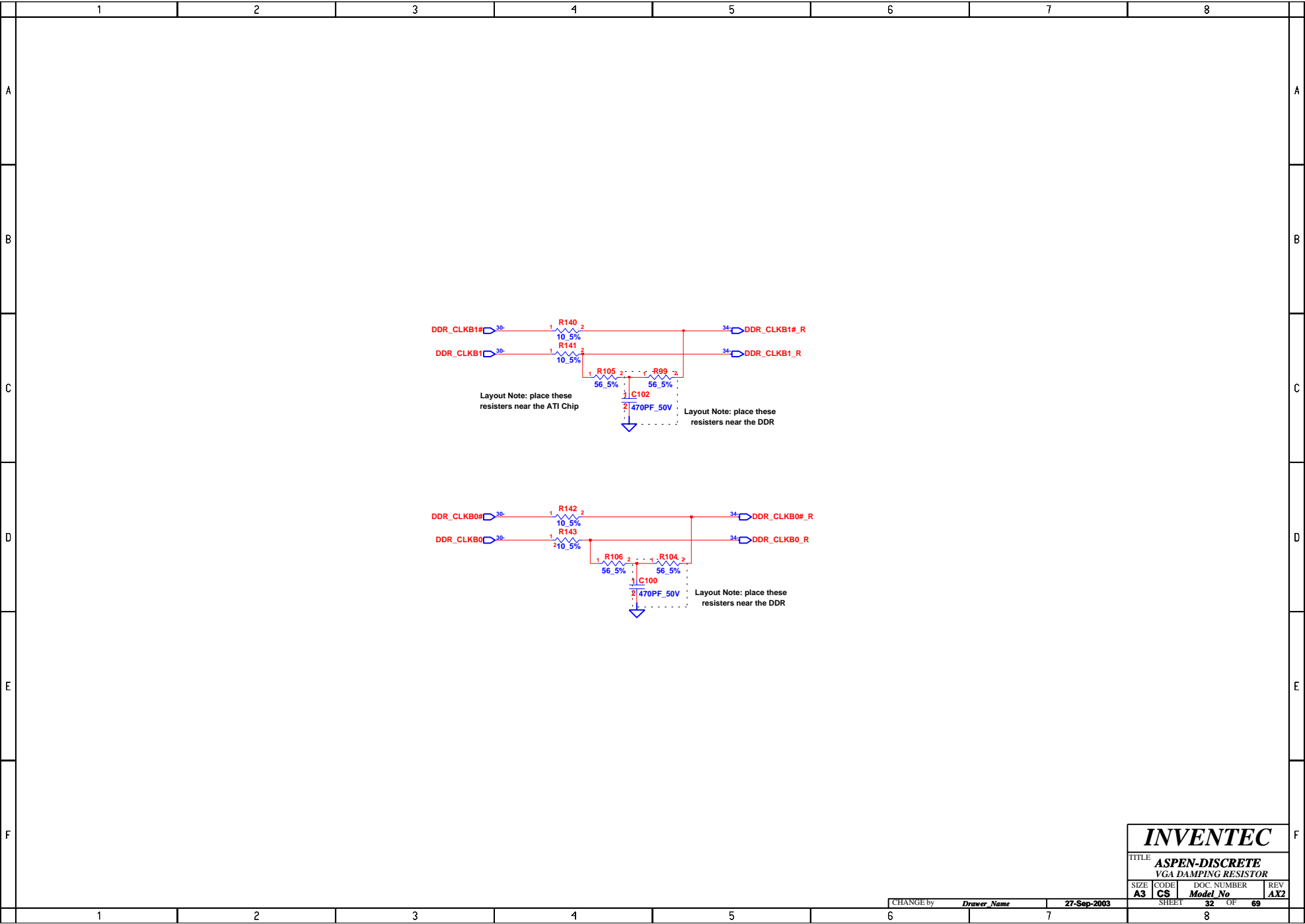












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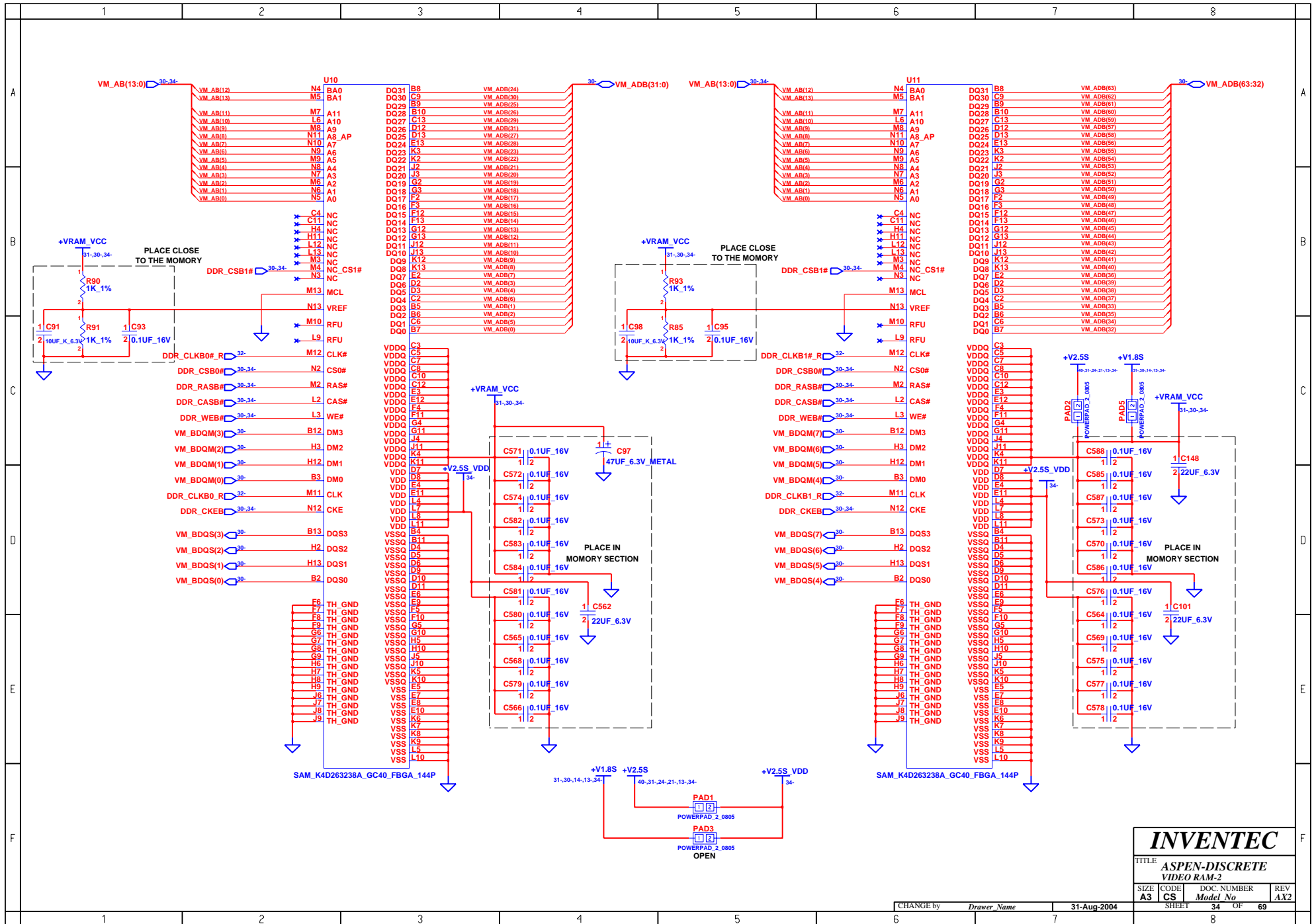
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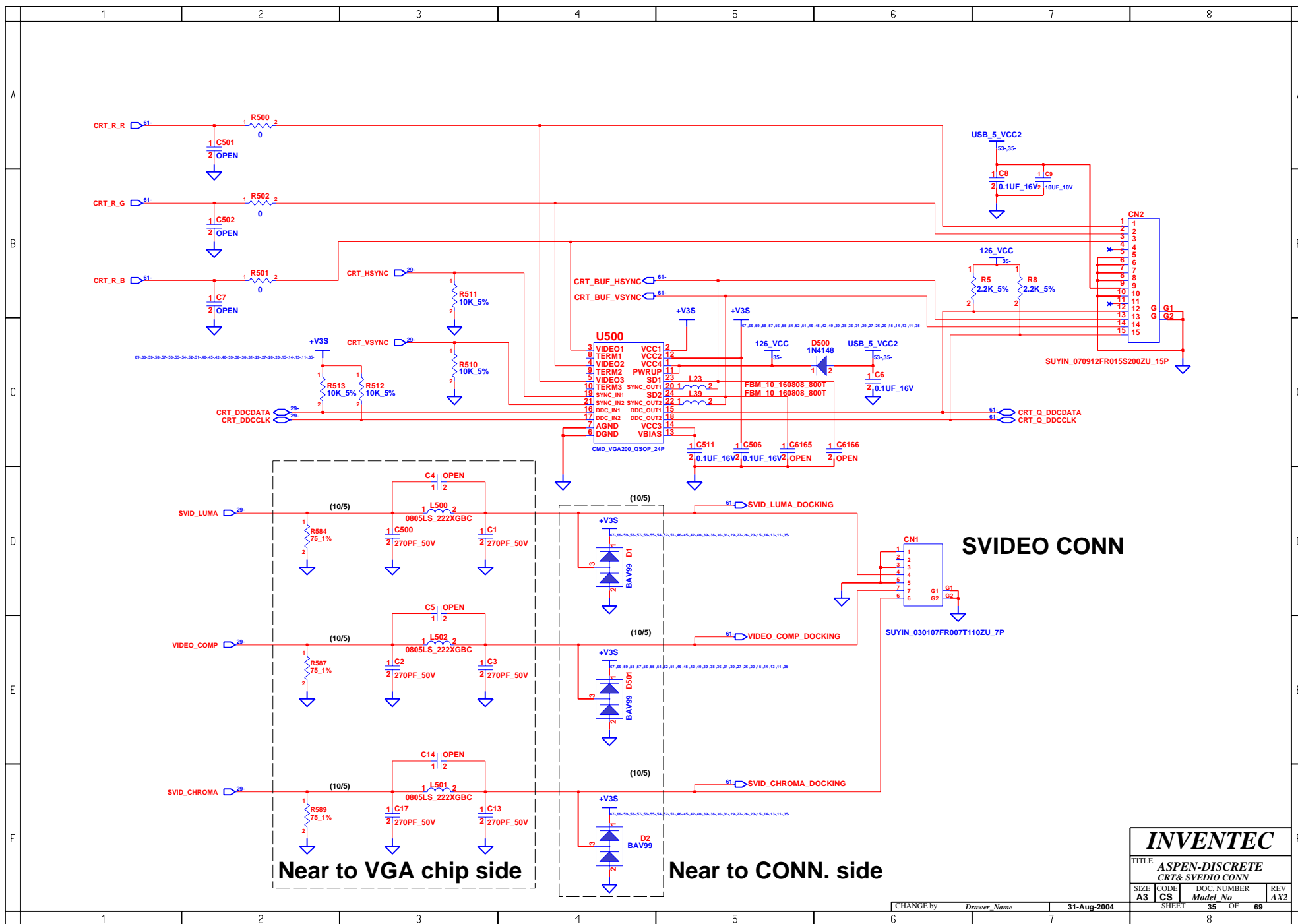
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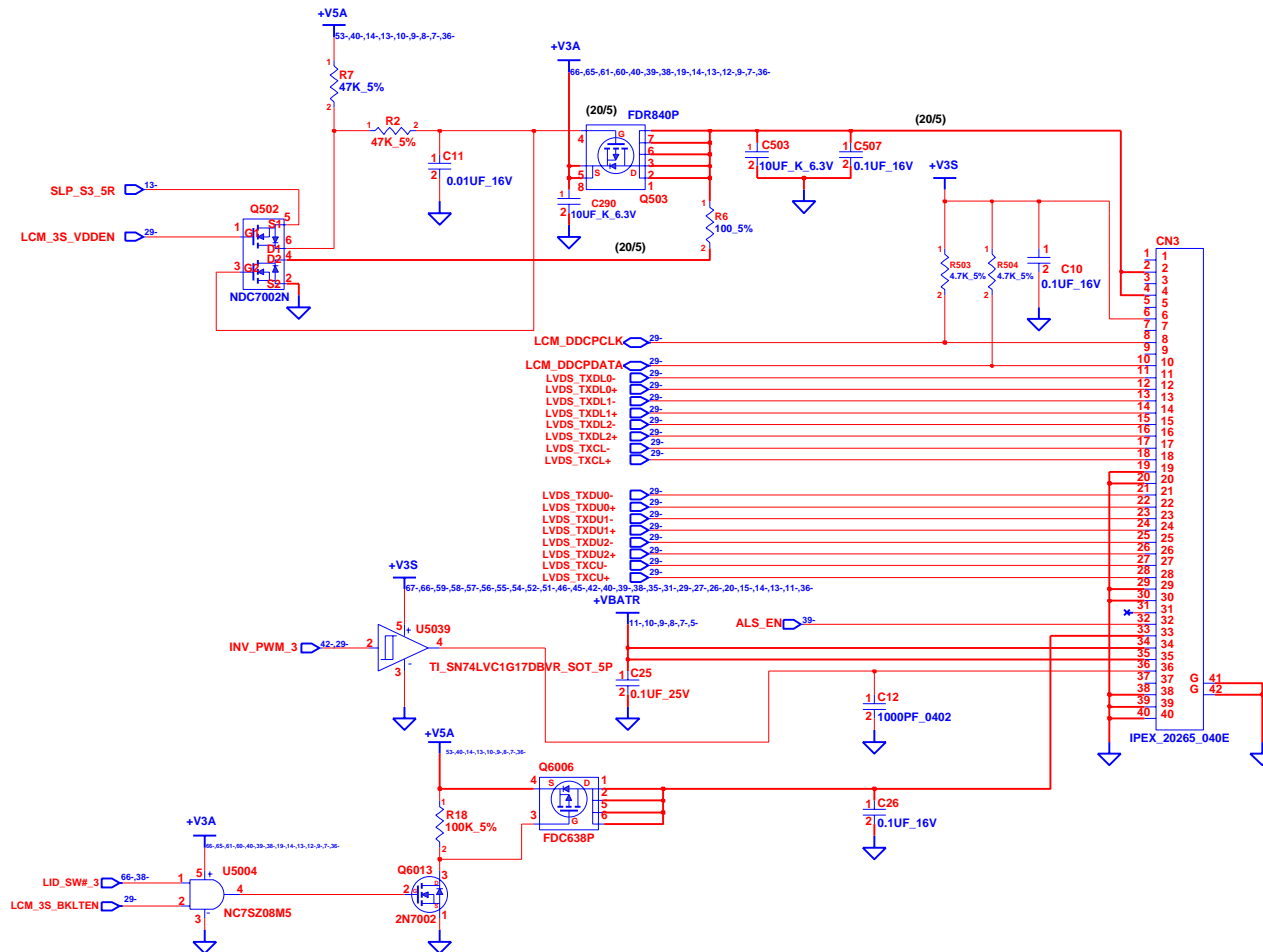
SHEET 32 OF 69

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B									B
C									C
D									D
E									E
F	<div><div>INVENTEC</div><div>TITLE ASPEN-DISCRETE BLANK</div><div><div>SIZE A3</div><div>CODE CS</div><div>DOC. NUMBER Model No</div><div>REV AX2</div></div><div><div>CHANGE by Drawer Name</div><div>31-Aug-2004</div><div>SHEET 33</div><div>OF 69</div><div>8</div></div></div>								F
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Place closed to connector



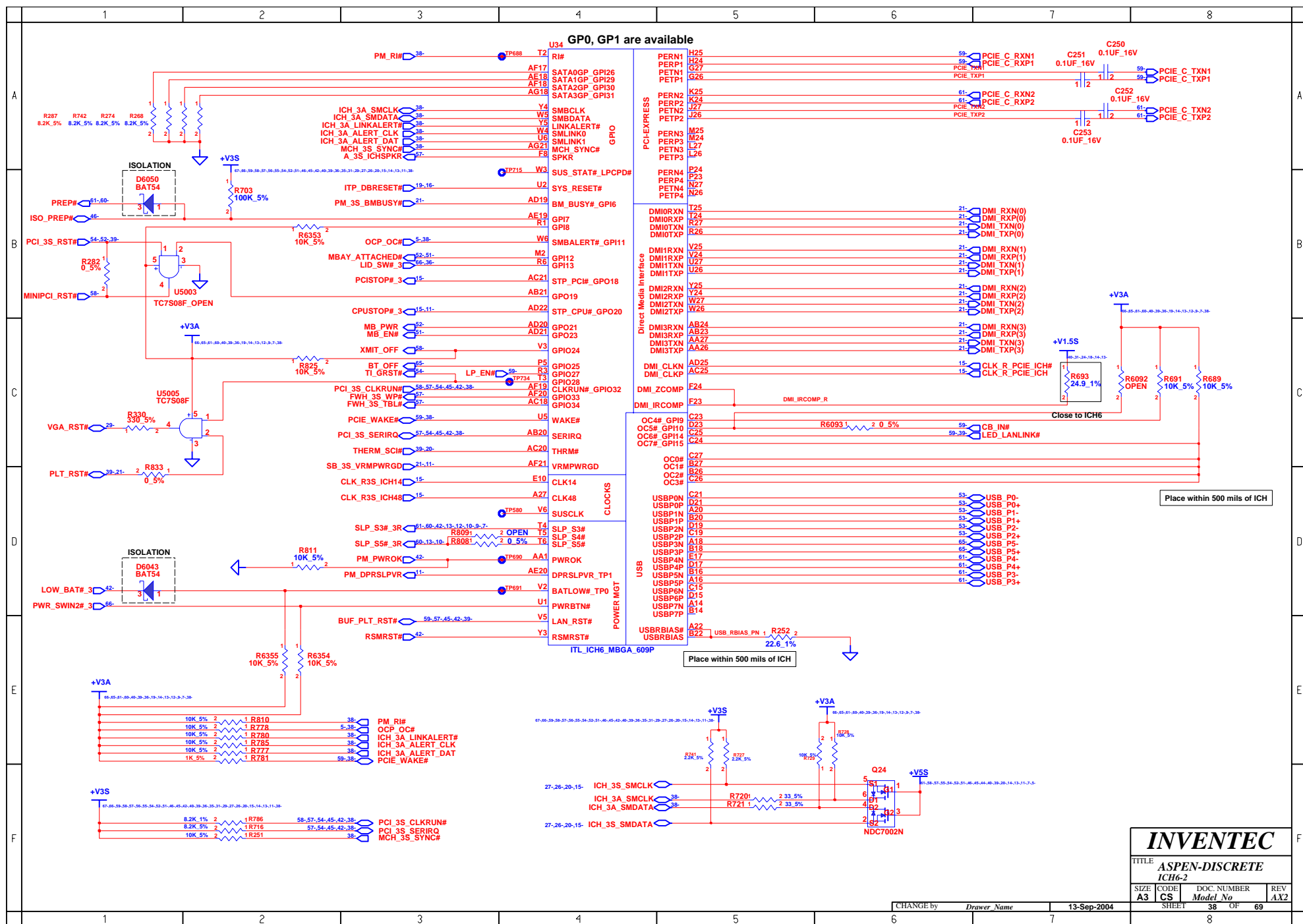
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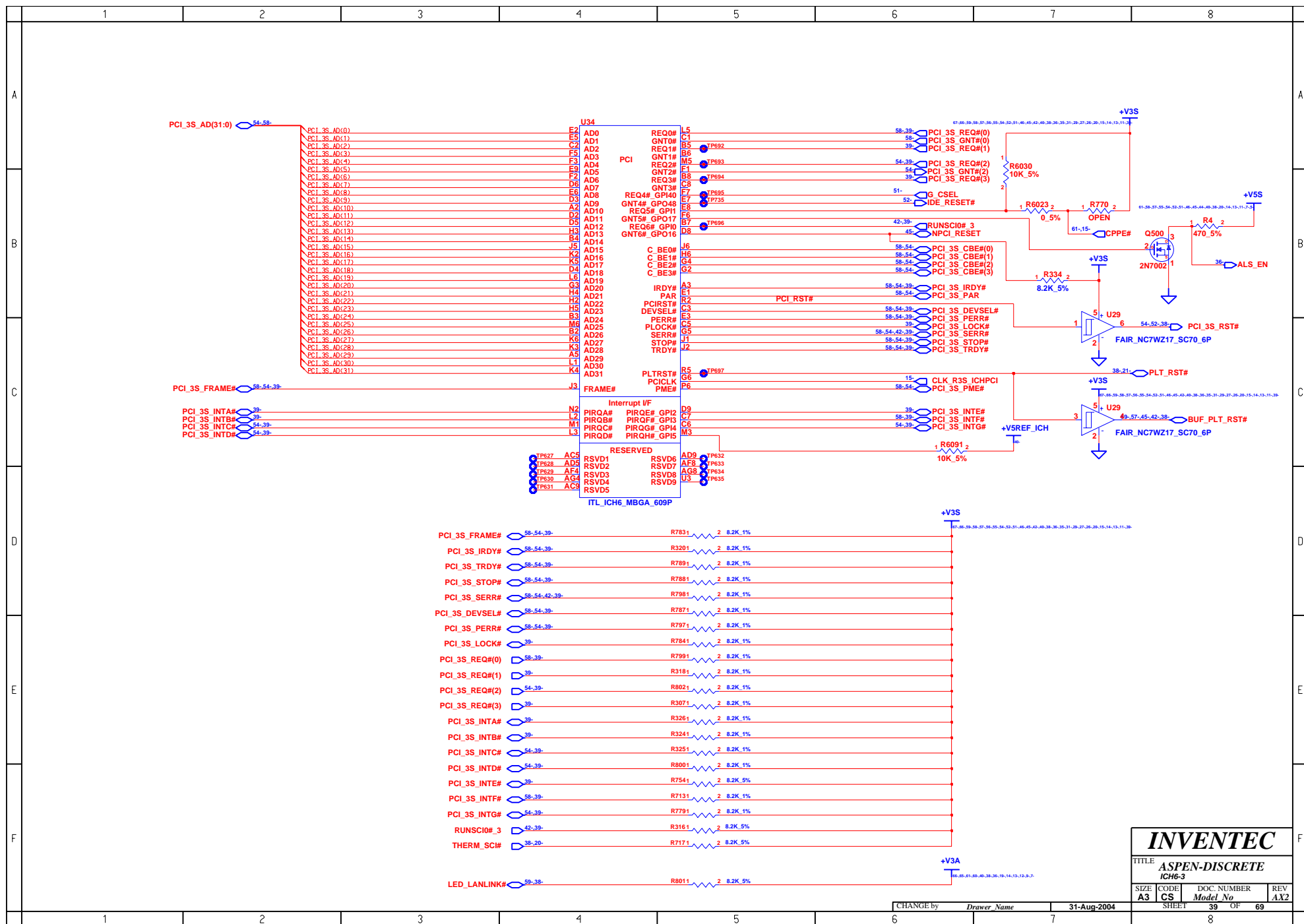
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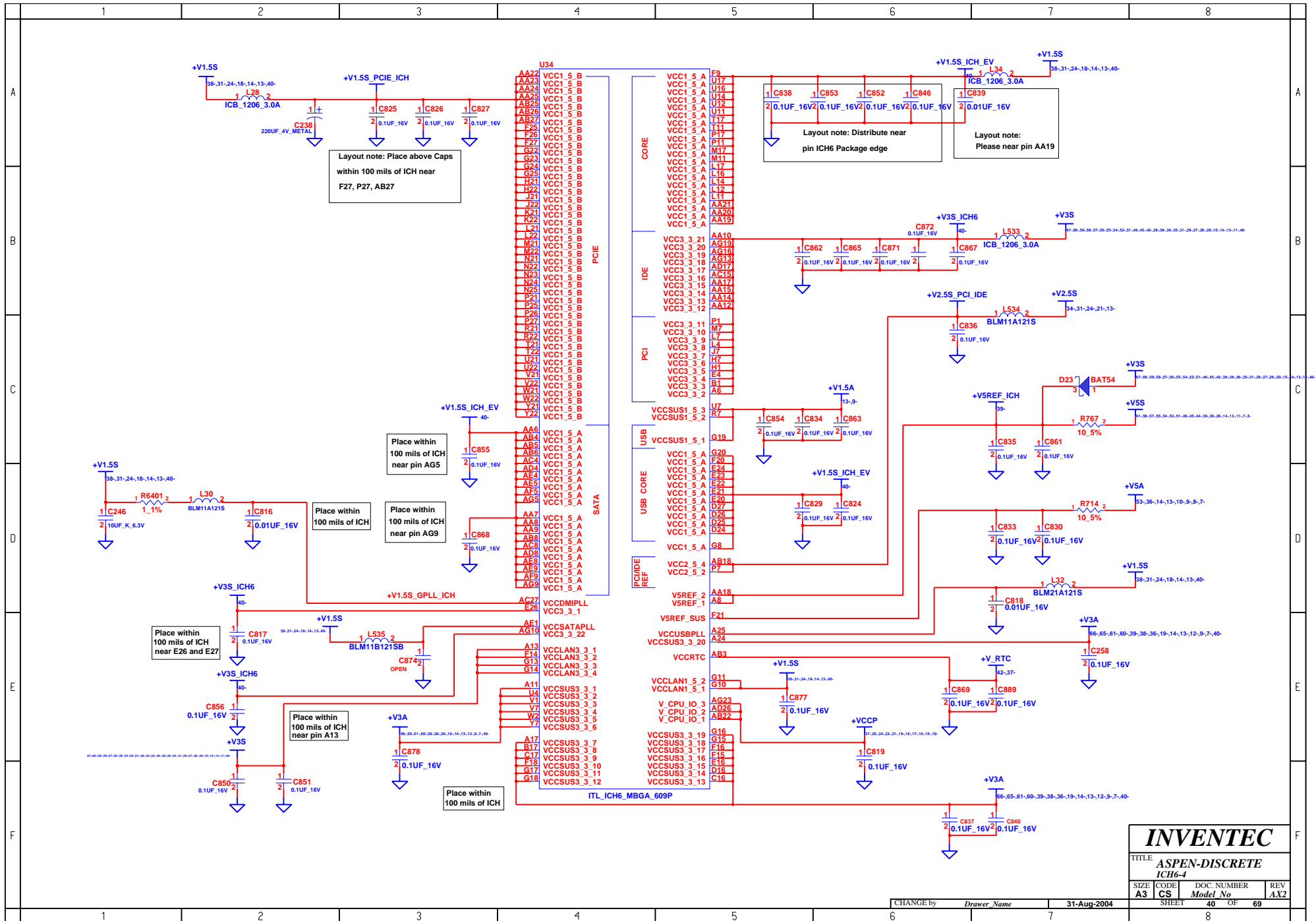
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawer Name* 31-Aug-2004

SHEET 36 OF 69







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A									A
B									B
C									C
D									D
E									E
F									F

U34

E27

V26

Y26

Y26

Y23

W27

W26

W24

W23

W1

V27

V26

V23

U25

U24

U23

U16

U13

T7

T27

T26

T23

T16

T15

T14

T13

T12

R4

R25

R24

R23

R17

R16

R15

R13

R12

R11

P22

P16

P15

P14

P13

P12

N7

N17

N16

N15

N14

N13

N12

N11

N1

M4

M27

M26

M23

M16

M15

M14

M13

M12

L25

L24

L23

L15

L13

K7

K27

K26

K23

K1

J5

J24

J23

H27

H26

H23

G9

G7

G21

G12

G1

VSS172

VSS171

VSS170

VSS169

VSS168

VSS167

VSS166

VSS165

VSS164

VSS163

VSS162

VSS161

VSS160

VSS159

VSS158

VSS157

VSS156

VSS155

VSS154

VSS153

VSS152

VSS151

VSS150

VSS149

VSS148

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VSS94

VSS93

VSS92

VSS91

VSS90

VSS89

VSS88

VSS87

F4

F22

F19

F17

E25

E18

E15

E14

D22

D20

D18

D13

D10

C1

C22

C20

C18

C14

B25

B24

B23

B21

B19

B15

B13

AG7

AG3

AG22

AG20

AG17

AG14

AG12

AF7

AF3

AF28

AF12

AF10

AF1

AE6

AE25

AE21

AE2

AE12

AE11

AE10

AD6

AD24

AD2

AD18

AD15

AD10

AD1

AC3

AC6

AC3

AC26

AC24

AC23

AC22

AC17

AC10

AB7

AB2

AB19

AB10

AA1

AA4

AA18

AA13

AA11

A9

A7

A4

A26

A23

A21

A18

A15

A12

A1

ITL_ICH6_MBG_609P

CHANGE by

Drawer Name

31-Aug-2004

SIZE

A3

CODE

CS

DOC. NUMBER

Model No

REV

AX2

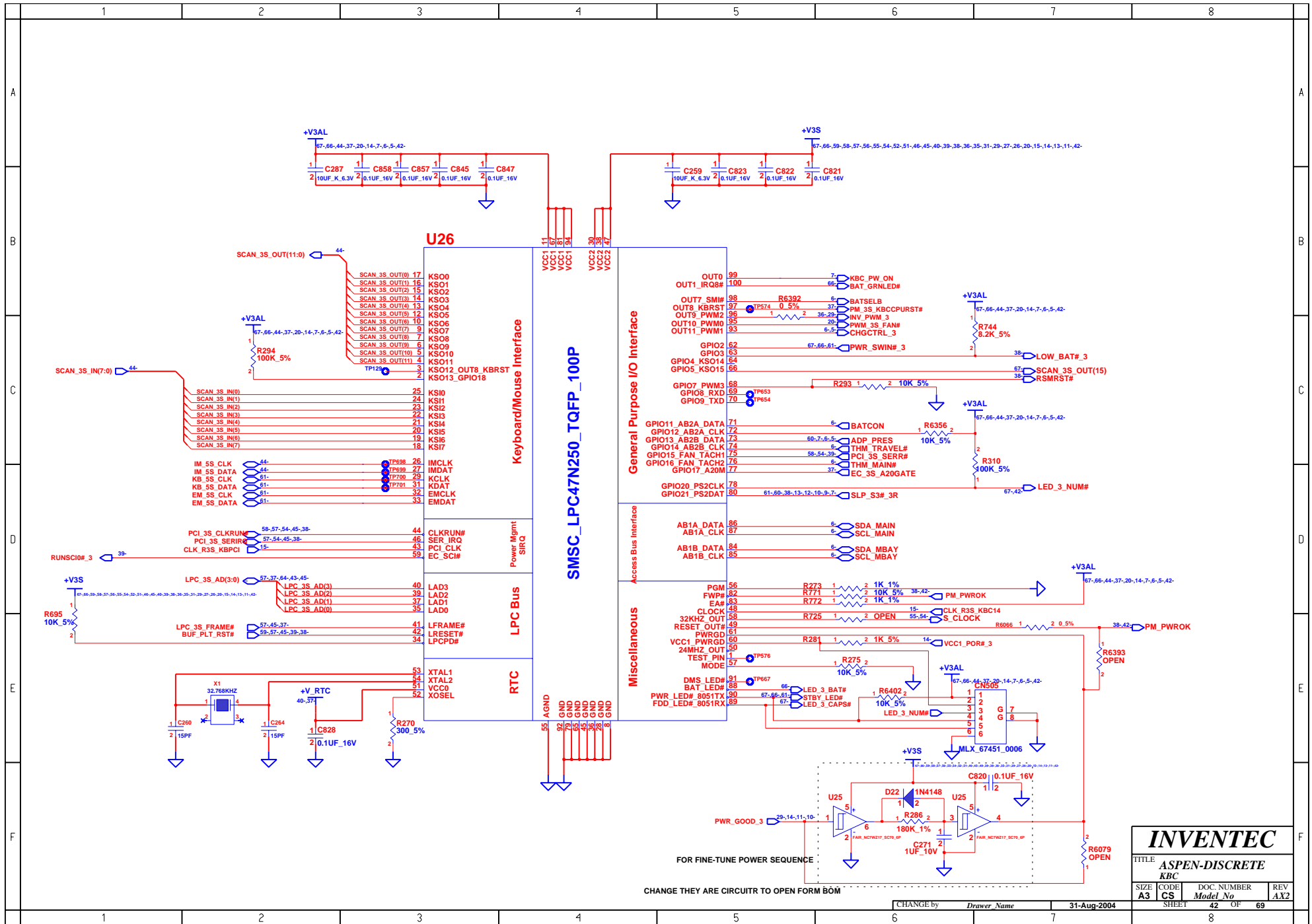
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OF

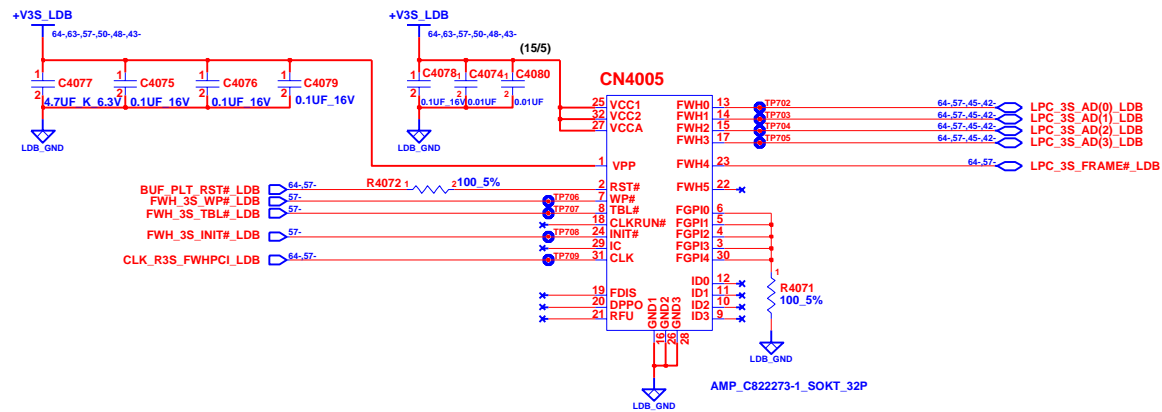
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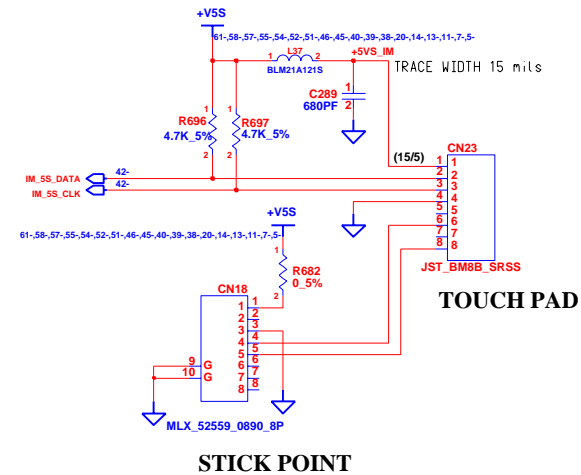
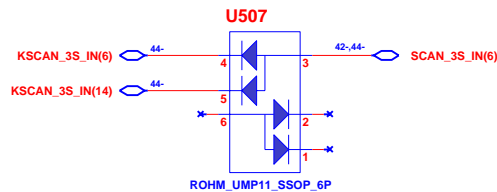
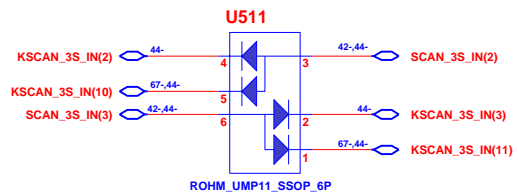
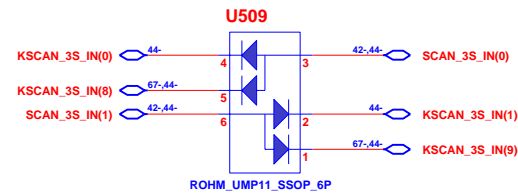
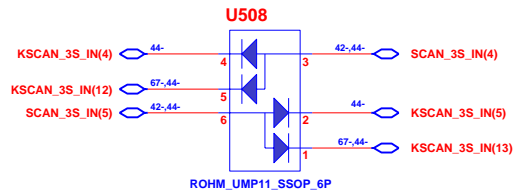
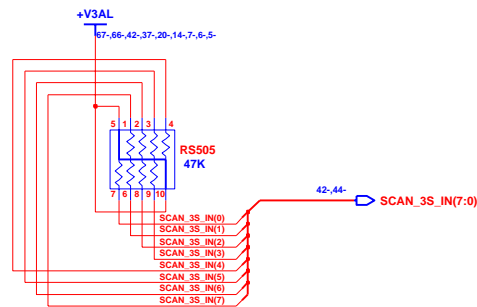
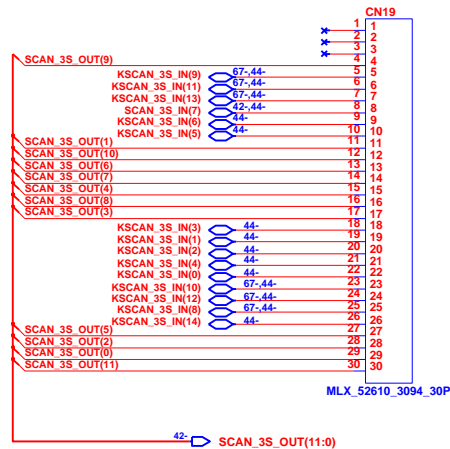
INVENTEC

TITLE			
ASPEN-DISCRETE			
KBC			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2
SHEET		42	OF 69



MDC , TCPA , OCP , FWH , AC97 B/B DAUGHTER BOARD

INVENTEC			
TITLE ASPEN-DISCRETE FWH			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drawer Name		31-Aug-2004	SHEET 43 OF 69



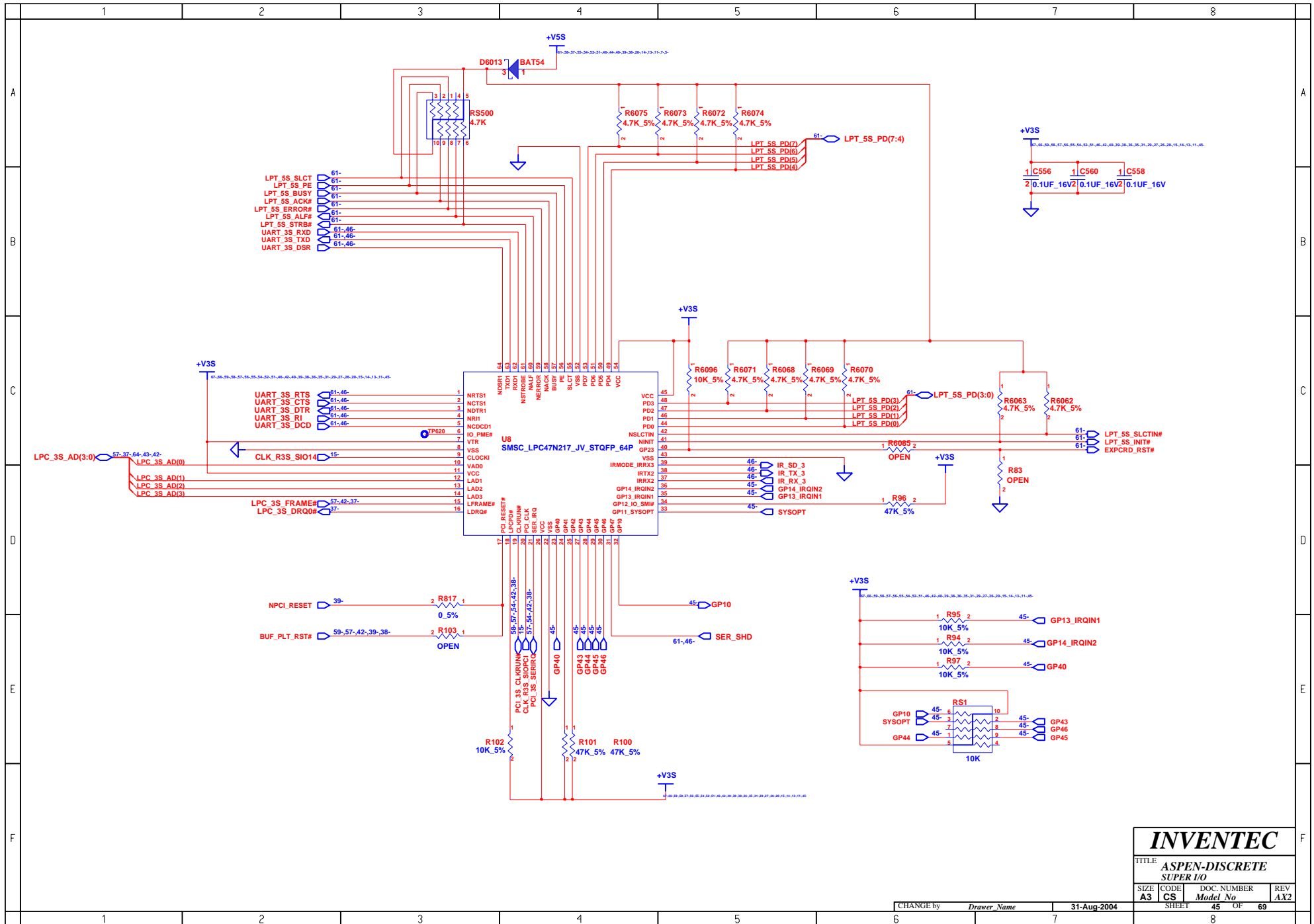
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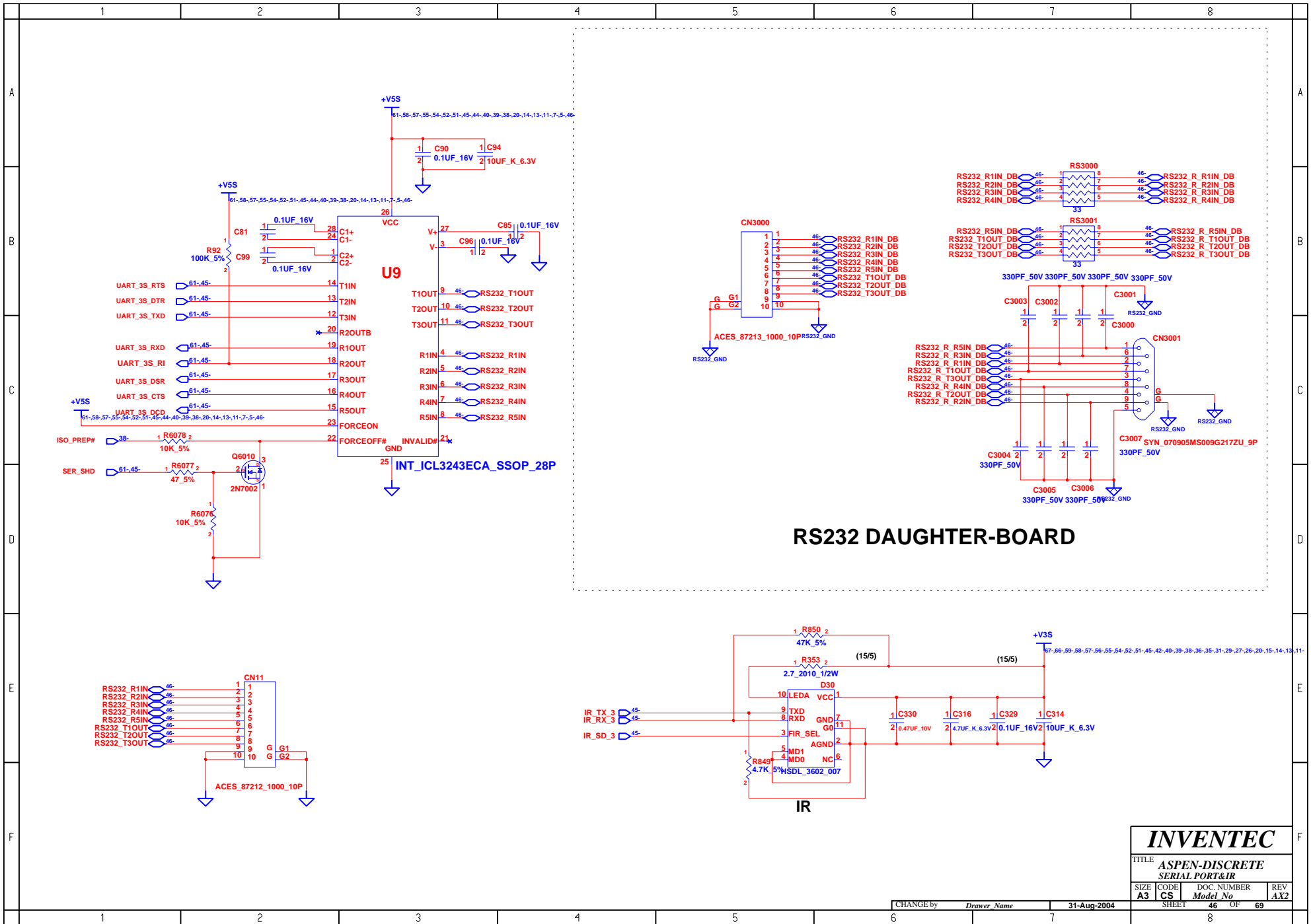
TITLE
ASPEN-DISCRETE
INT.KBC/POINT DEVICES

SIZE CODE DOC. NUMBER REV
A3 CS Model No **AX2**

CHANGE by *Drawer Name* 31-Aug-2004

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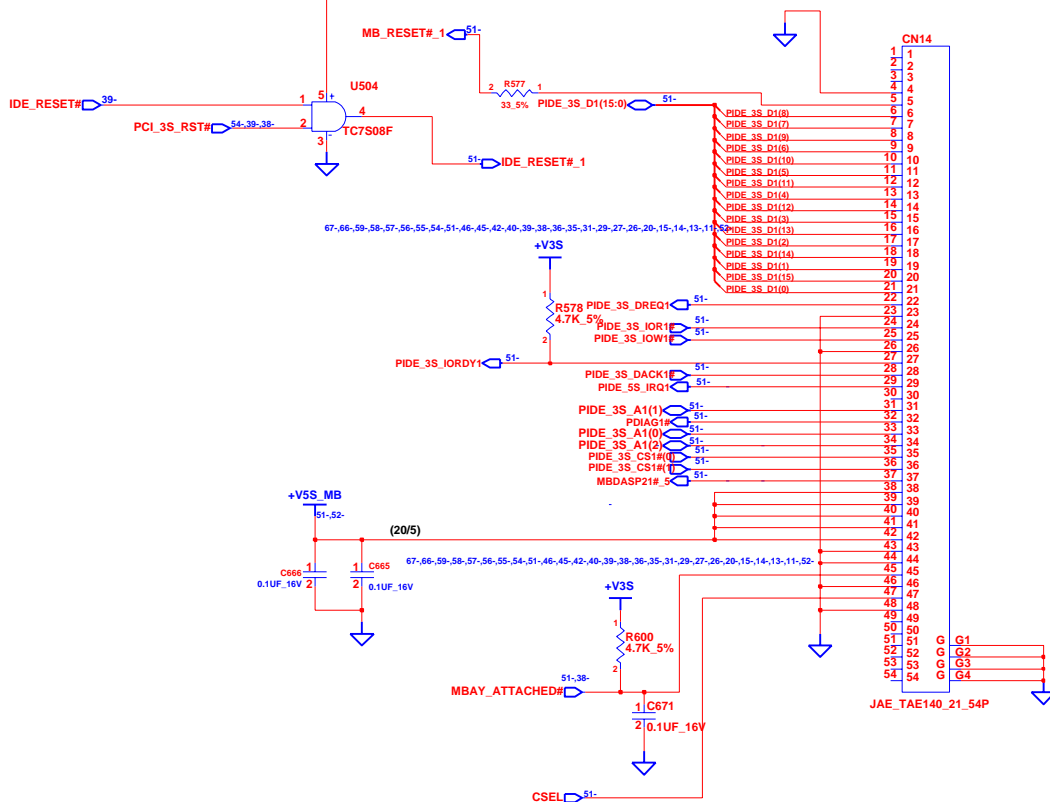




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B									B
C									C
D									D
E									E
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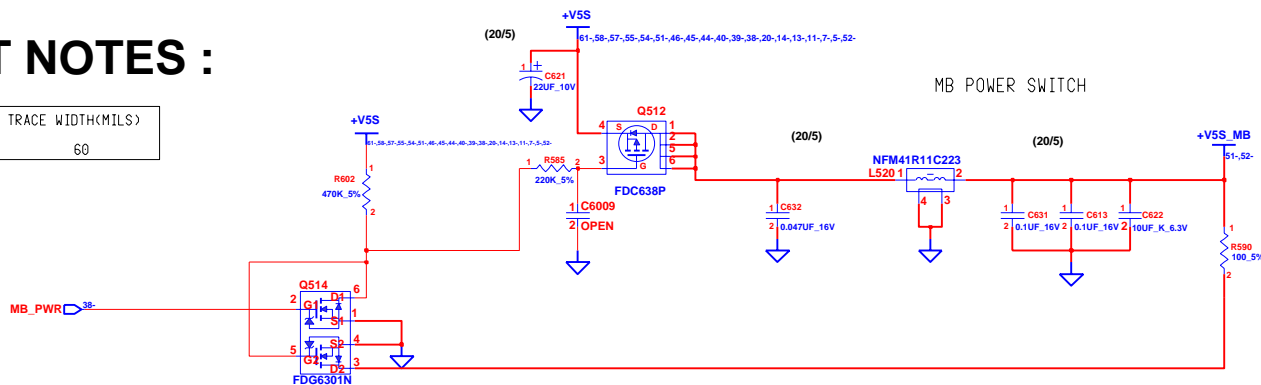
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TITLE ASPEN-DISCRETE PARALLER PORT			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drawer Name		31-Aug-2004	
SHEET		47	OF 69

LAYOUT NOTE : WILL BE CHANGE PUSH CONN 54 PIN



LAYOUT NOTES :

SIGNAL NAME	TRACE WIDTH(MILS)
+V5S_MB	60



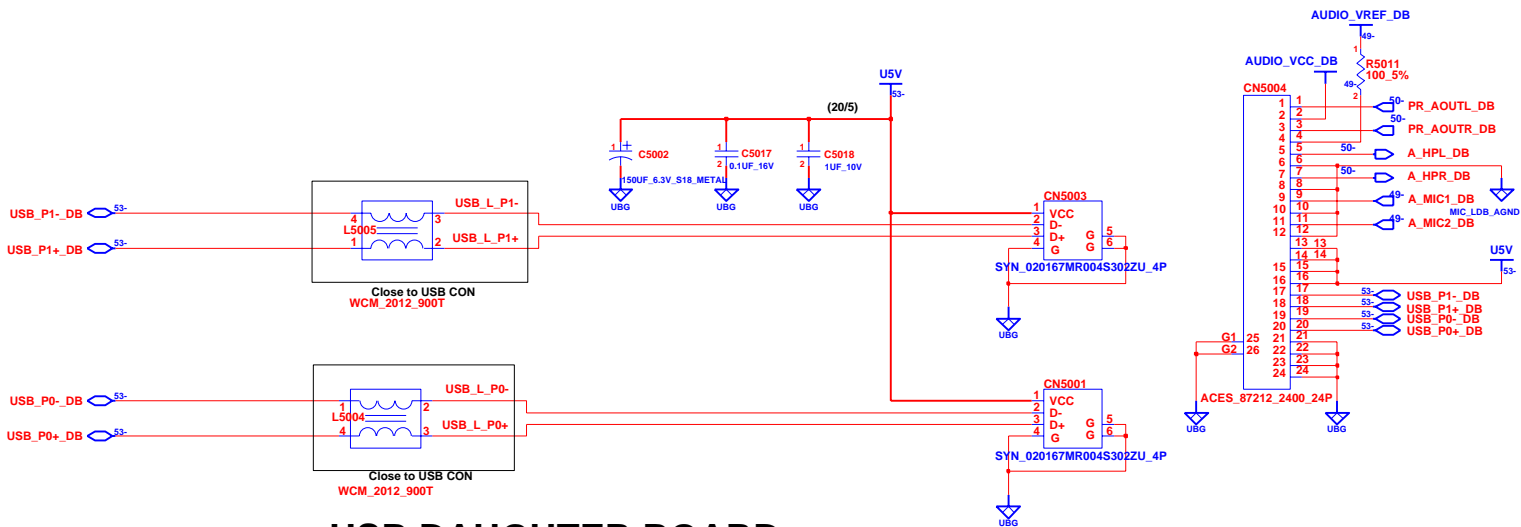
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TITLE
ASPEN-DISCRETE
MULTIBAY CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

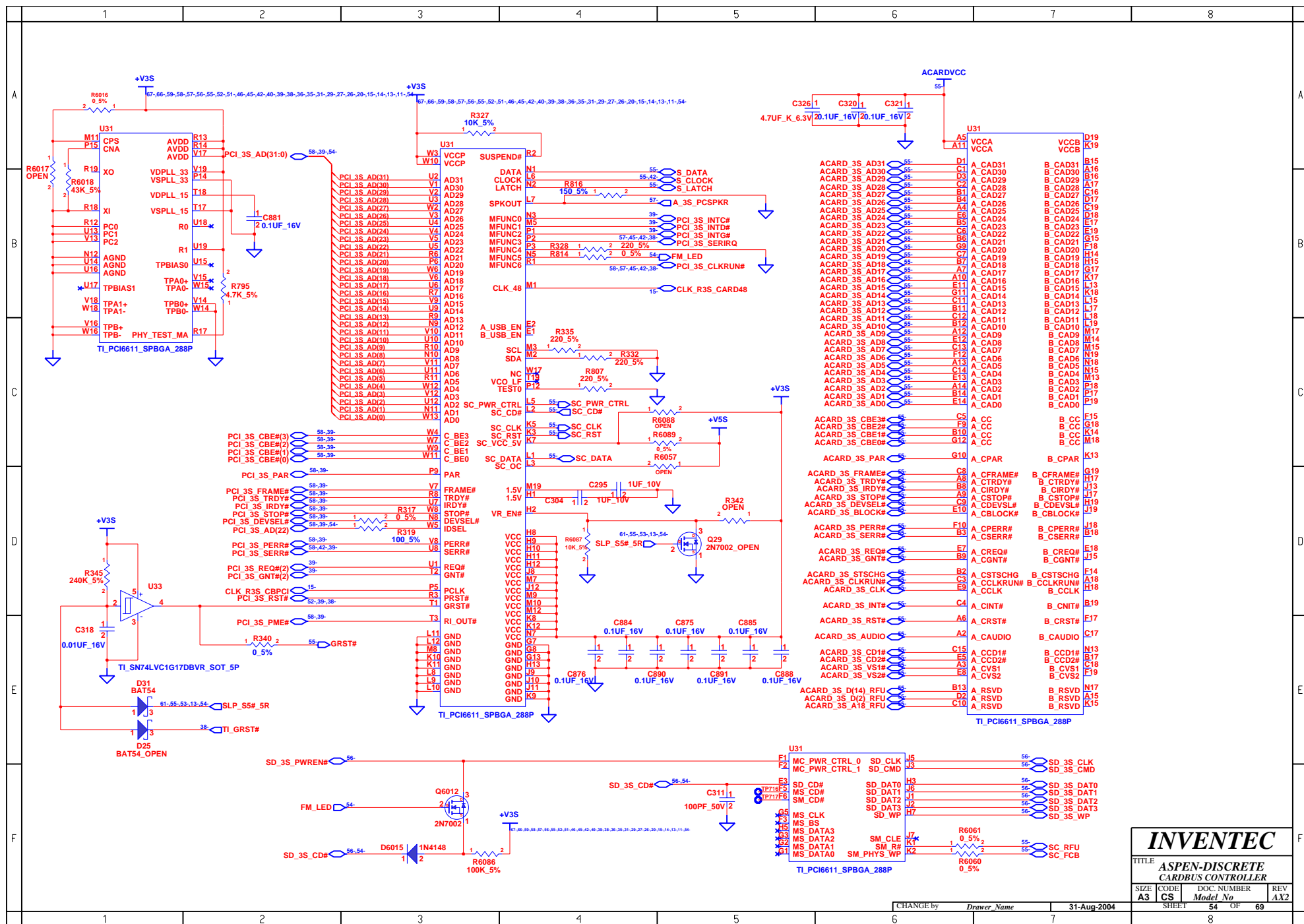
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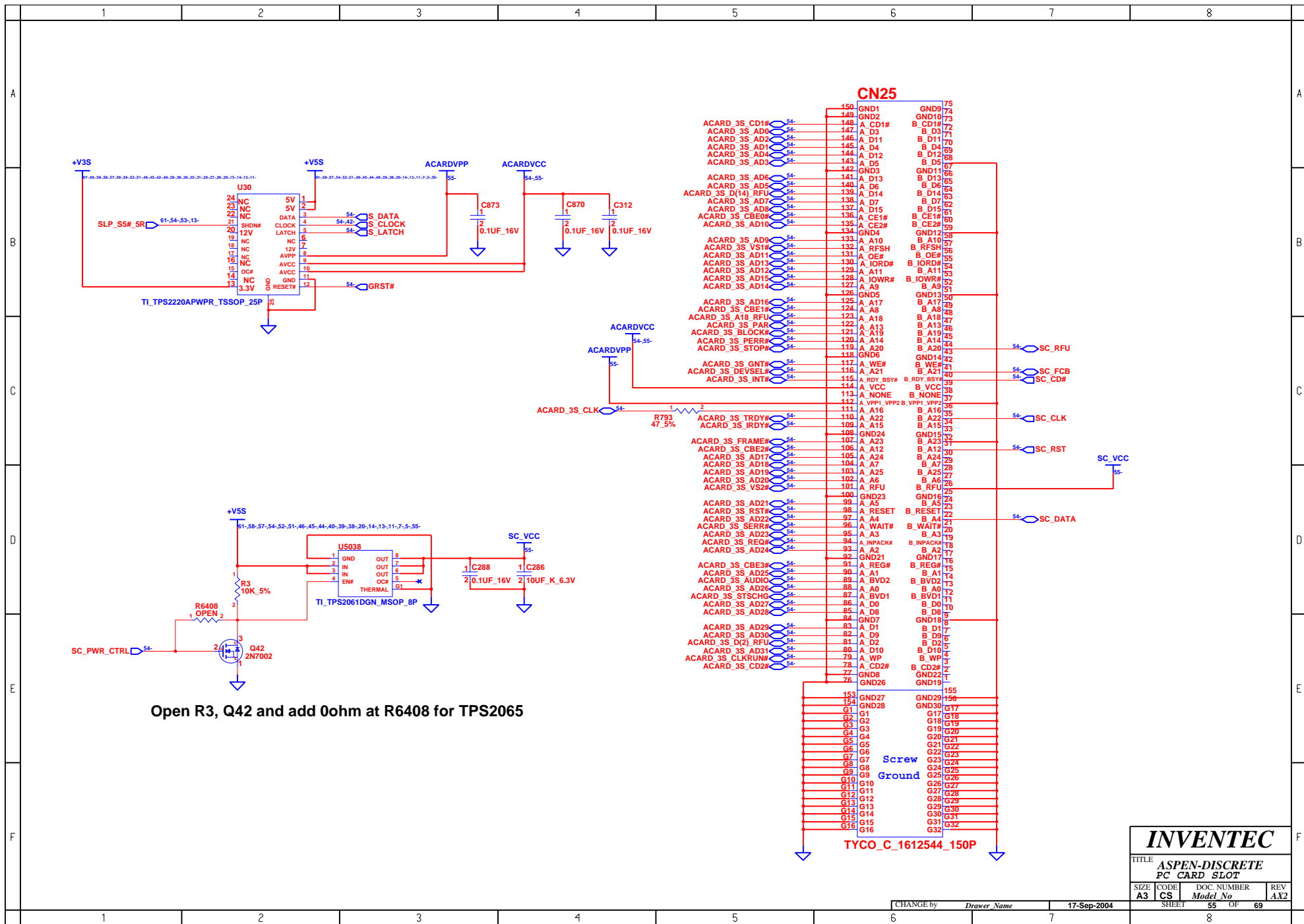
SHEET 52 OF 69

[illegible]

TITLE	ASPEN-DISCRETE USB&IR CONN
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SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX2
SHEET 53 OF 69			



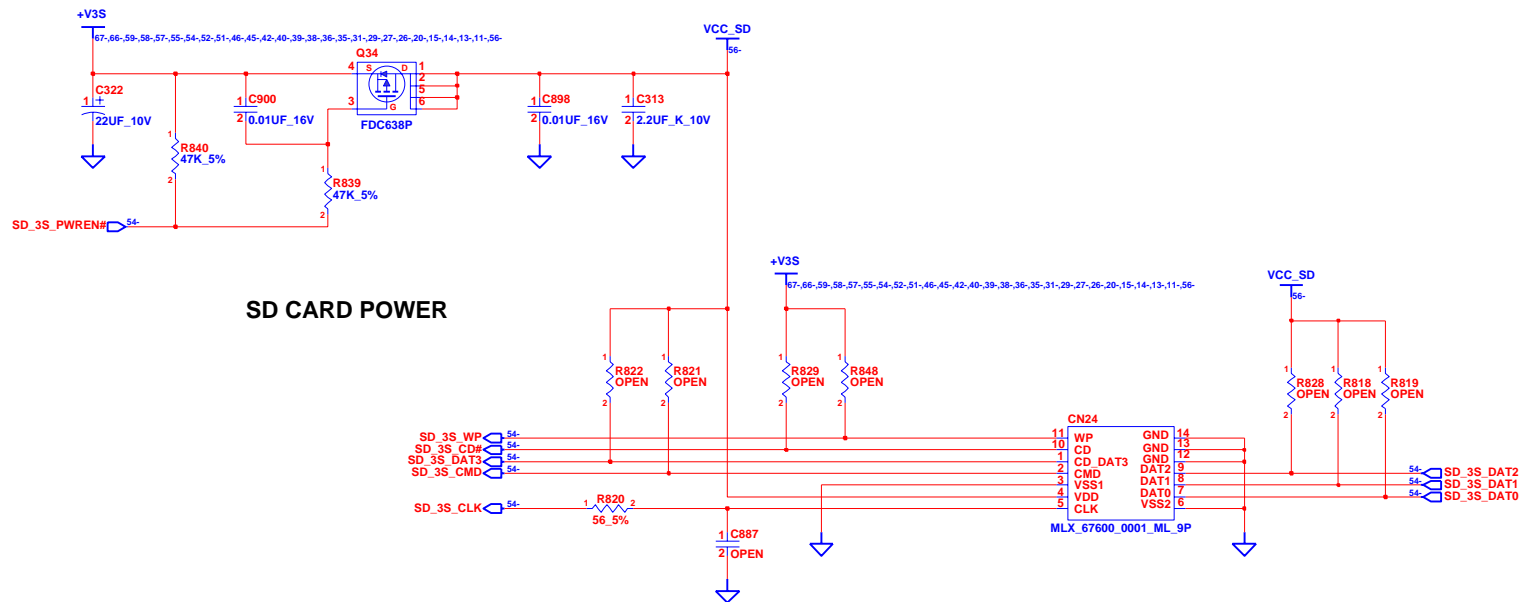


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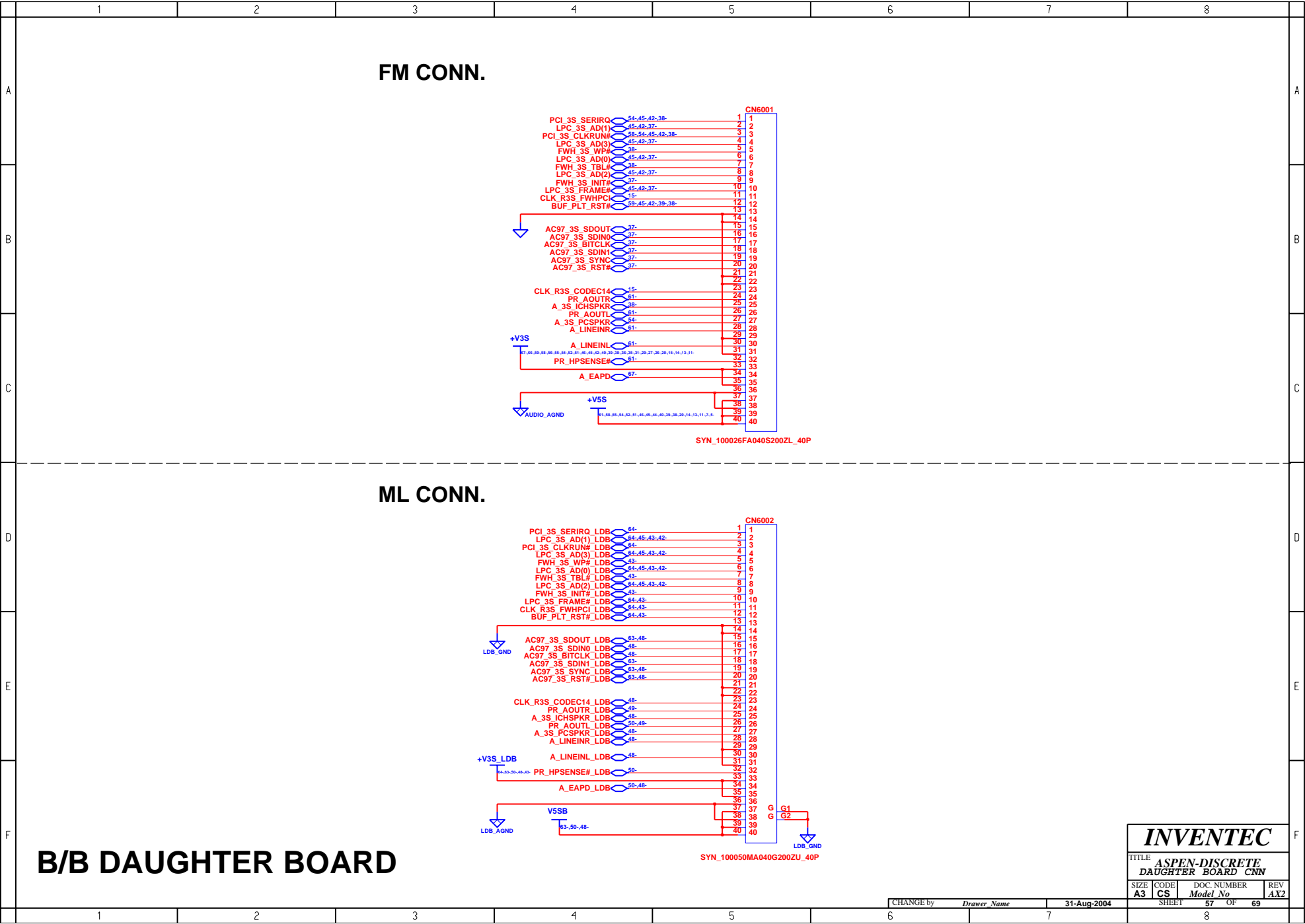
TITLE
**ASPEN-DISCRETE
PC CARD SLOT**

SIZE CODE DOC. NUMBER REV
A3 CS Model No AX2

SHEET 55 OF 69

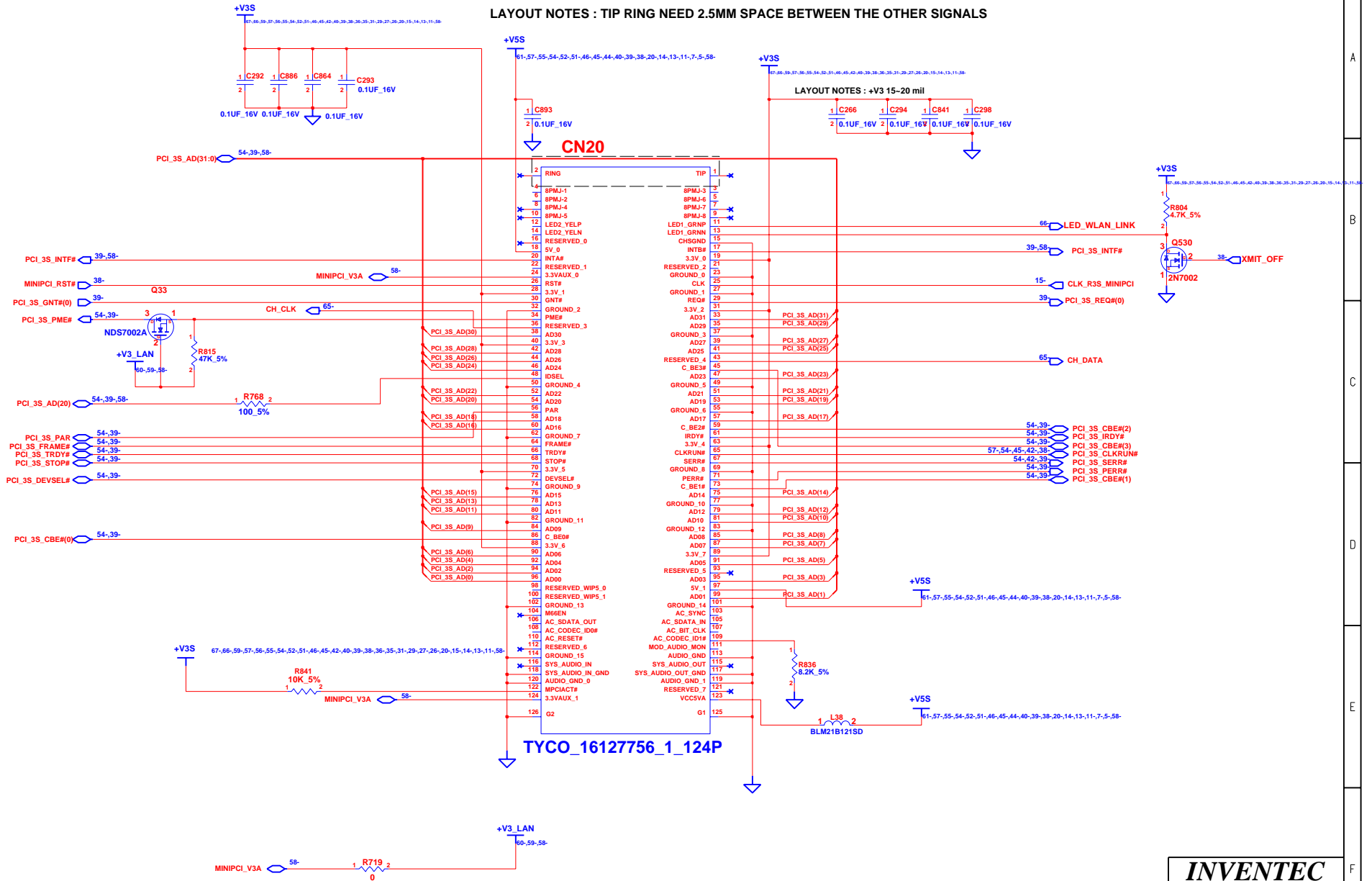


INVENTEC			
TITLE ASPEN-DISCRETE SD CARD CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drawer Name		31-Aug-2004	SHEET 56 OF 69



LAYOUT NOTES : TIP RING NEED 2.5MM SPACE BETWEEN THE OTHER SIGNALS

LAYOUT NOTES : +V3 15-20 mil



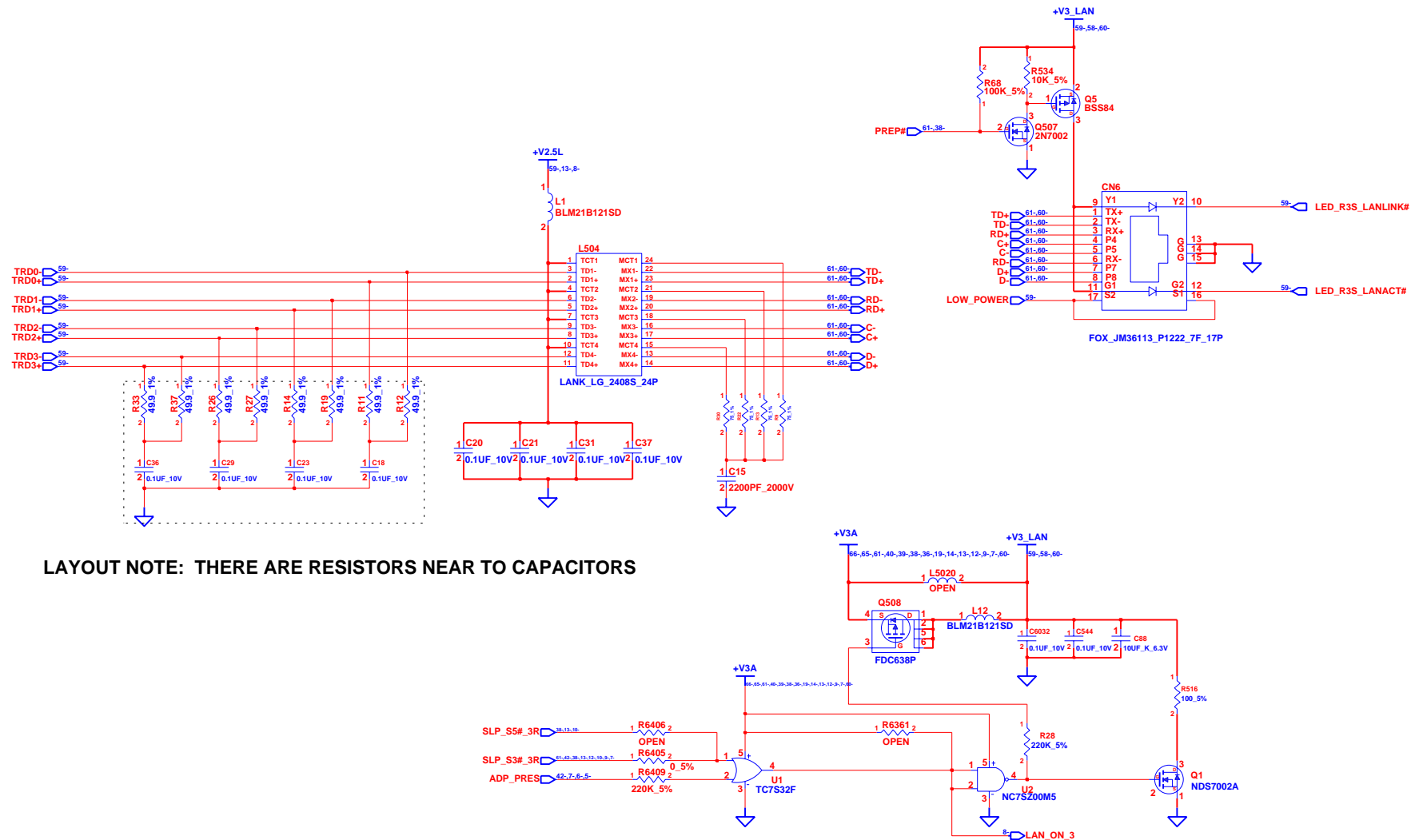
INVENTEC

TITLE
ASPEN-DISCRETE
MINIPCI CONN

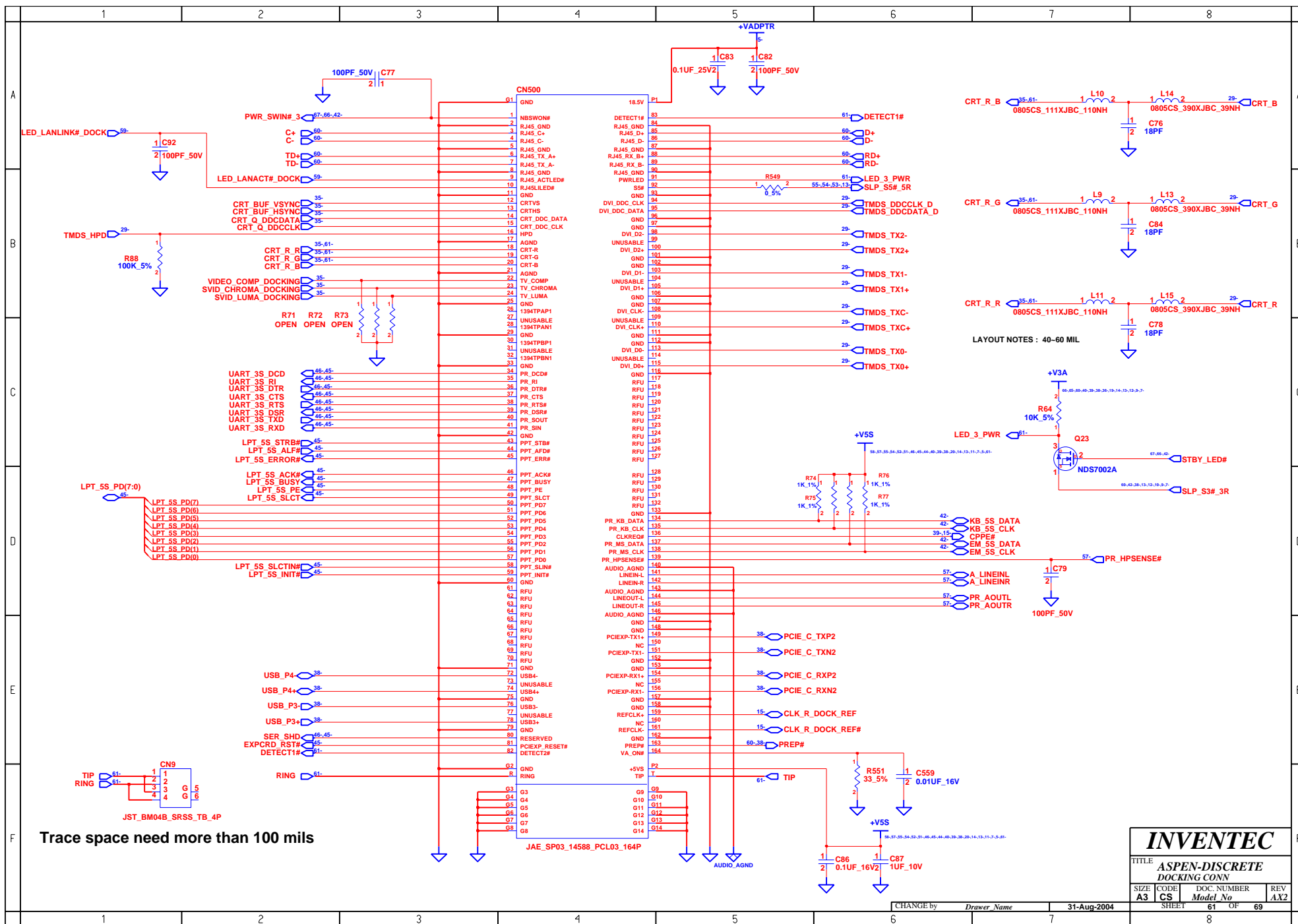
SIZE CODE DOC. NUMBER REV
A3 CS Model No AX2

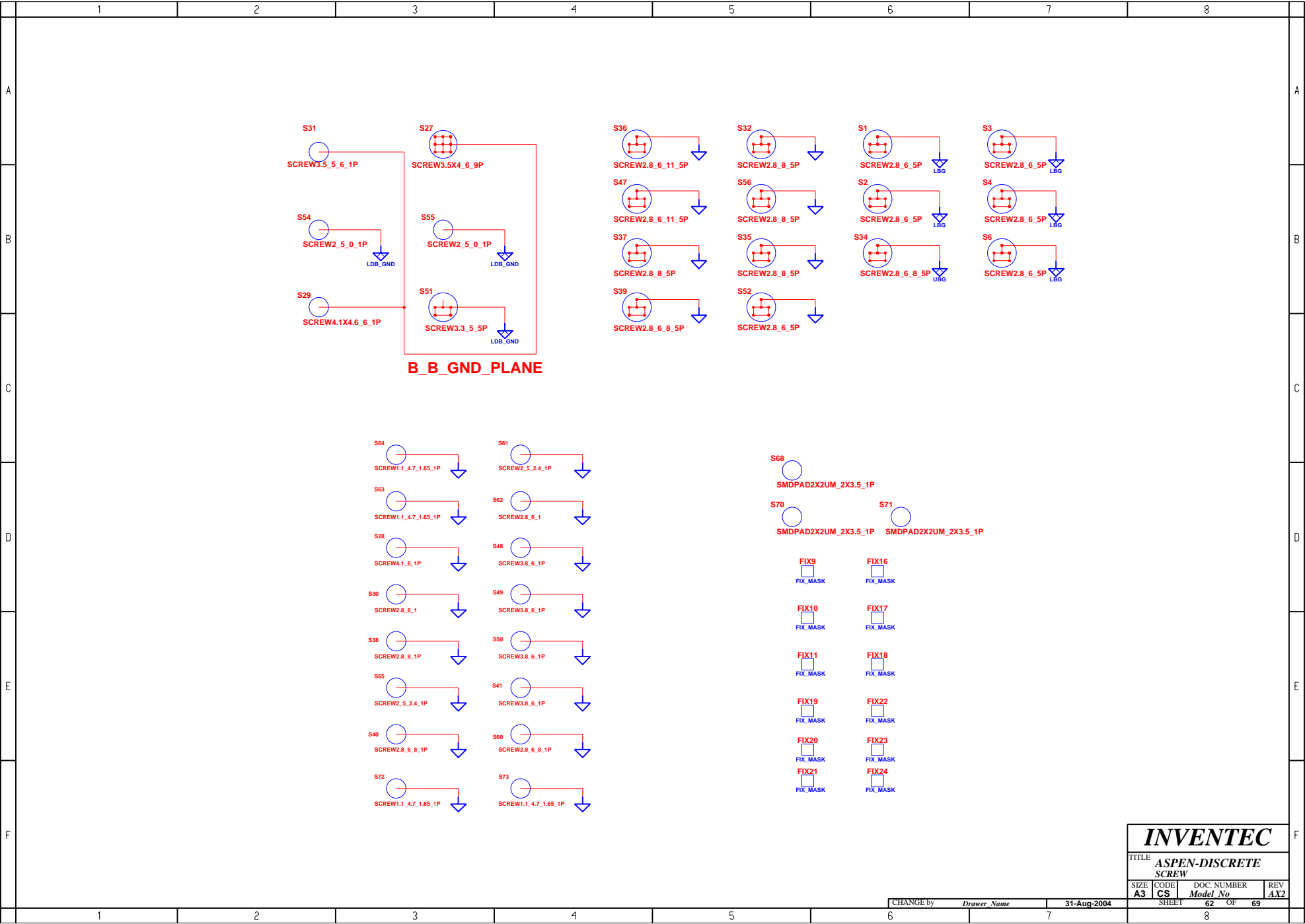
CHANGE by Drawer Name 31-Aug-2004

SHEET 58 OF 69



INVENTEC			
TITLE ASPEN-DISCRETE LAN RJ45 CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
SHEET 60 OF 69			





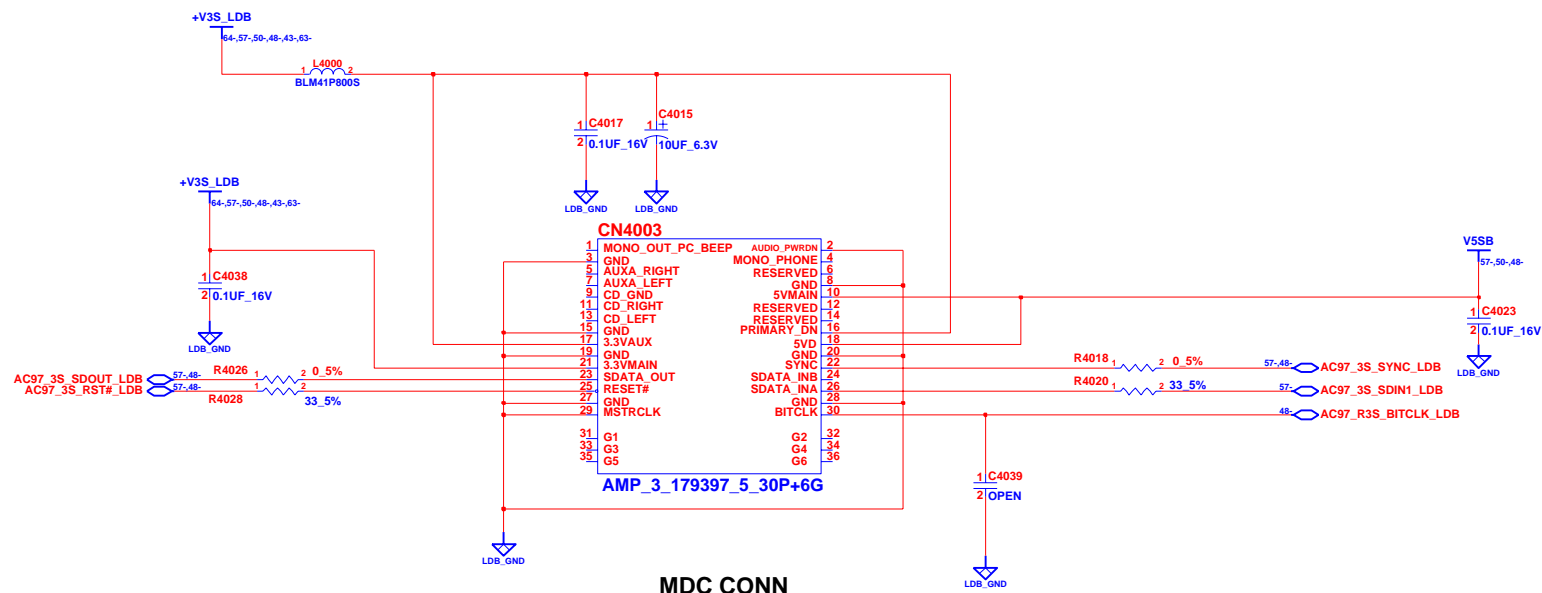
INVENTEC

TITLE
**ASPEN-DISCRETE
SCREW**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

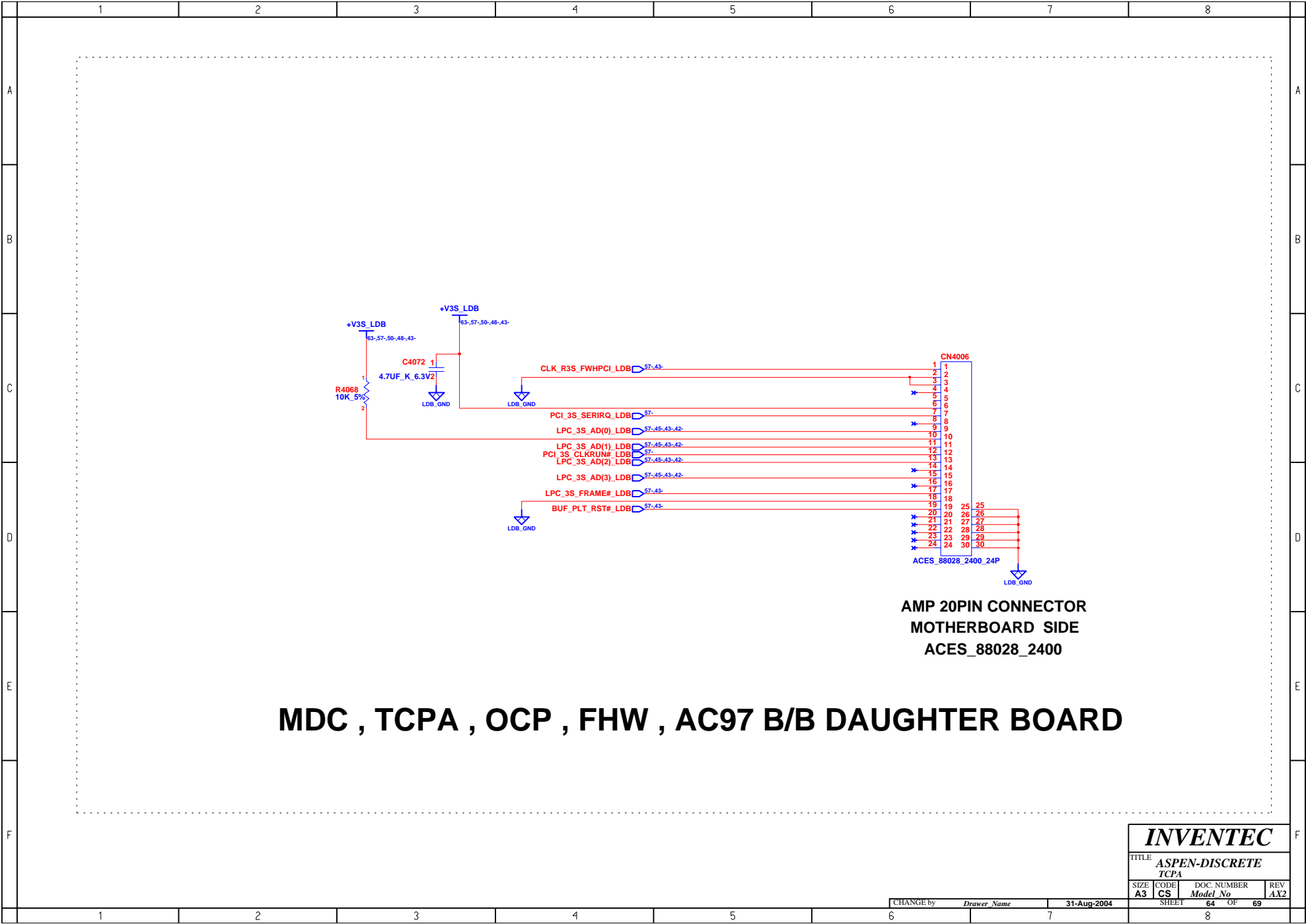
CHANGE by *Drawer Name* 31-Aug-2004

SHEET 62 OF 69



MDC , TCPA , OCP , FHW , AC97 B/B DAUGHTER BOARD

INVENTEC			
TITLE ASPEN-DISCRETE			
MDC CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drawer Name		31-Aug-2004	SHEET 63 OF 69
		7	8



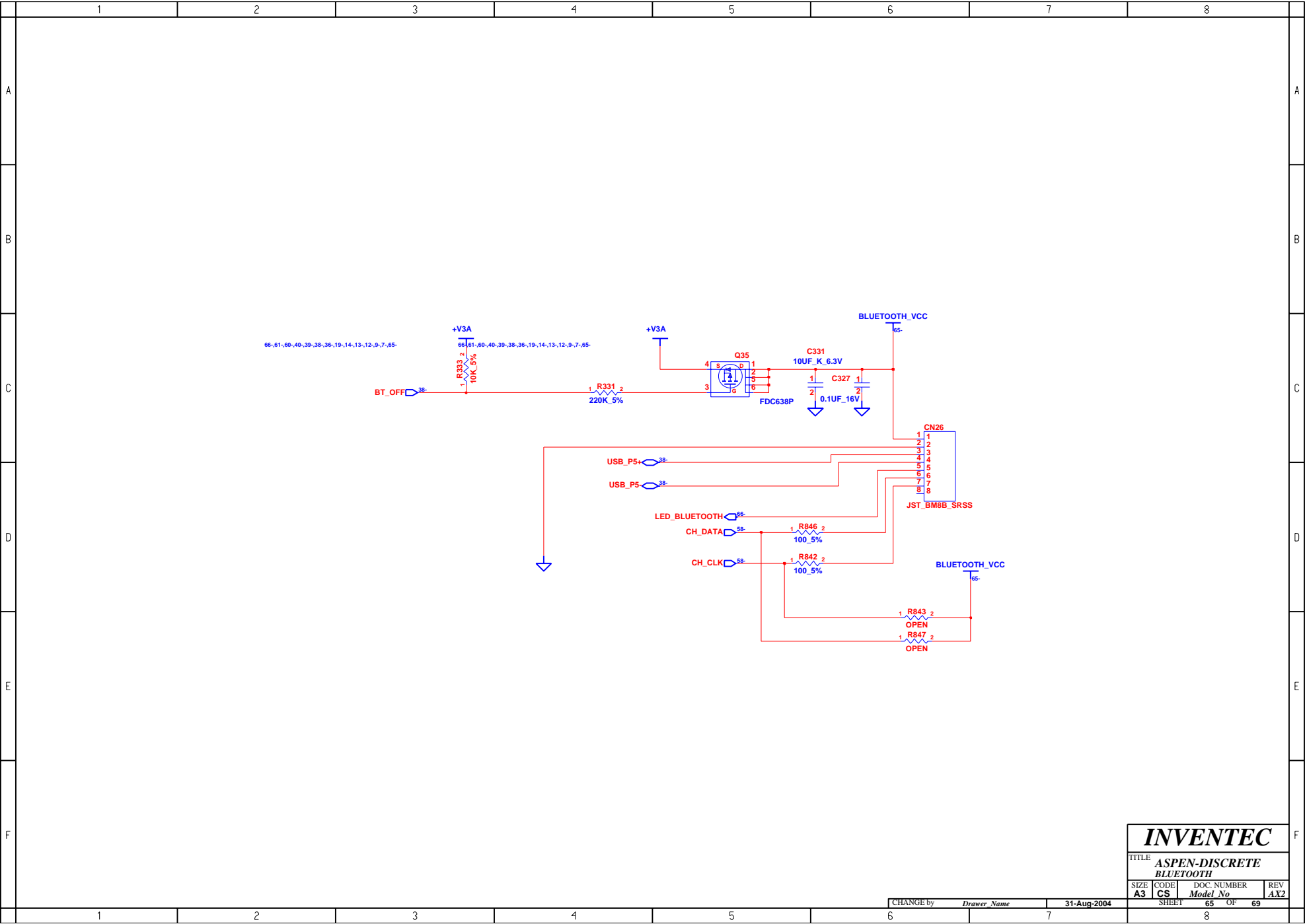
INVENTEC

TITLE
ASPEN-DISCRETE
TCPA

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

SHEET 64 OF 69

CHANGE by *Drawer Name* 31-Aug-2004

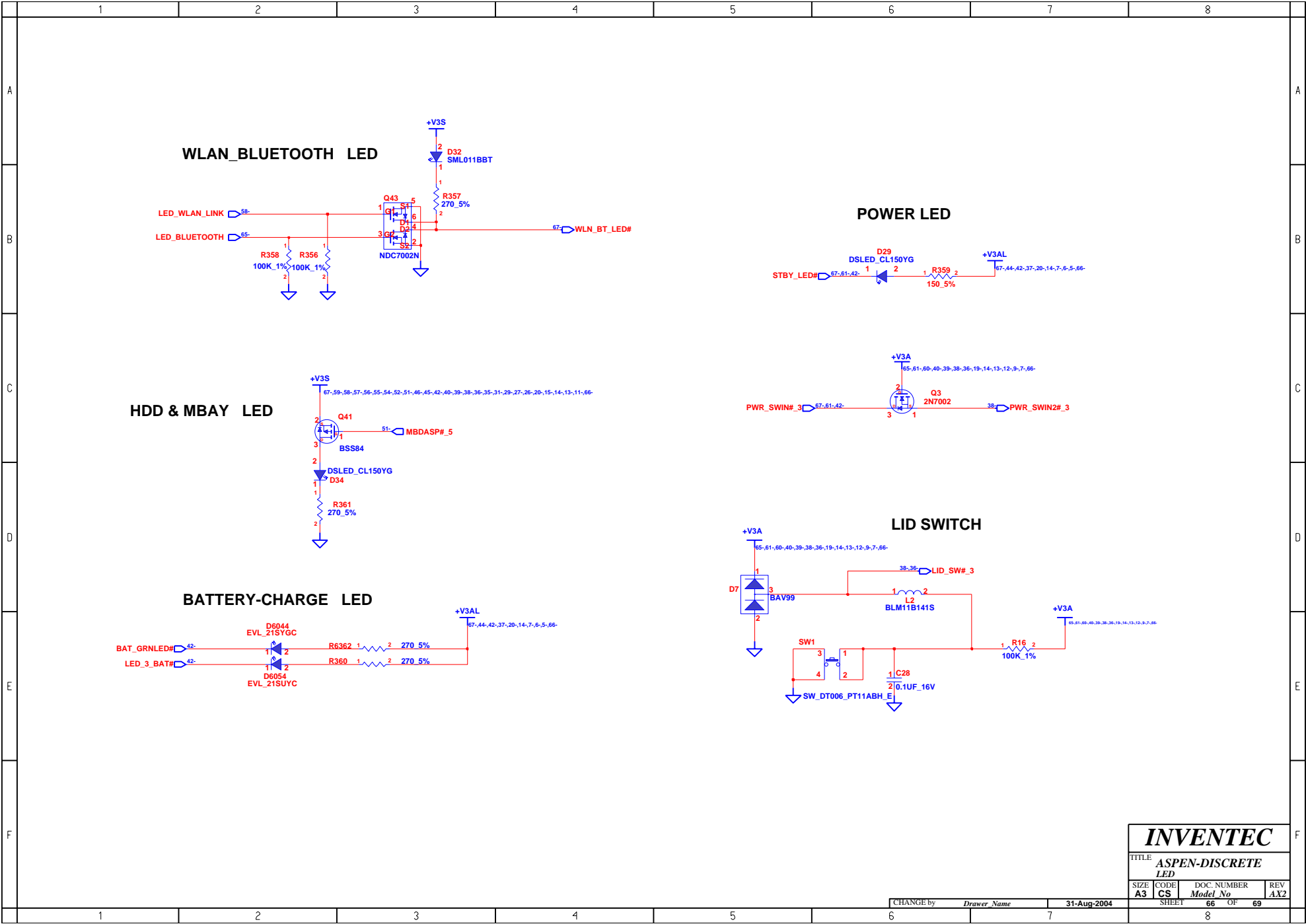


INVENTEC

TITLE
**ASPEN-DISCRETE
BLUETOOTH**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by	Drawer Name	31-Aug-2004	SHEET	65	OF	69
				8		



INVENTEC

TITLE
ASPEN-DISCRETE
LED

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
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CHANGE by *Drawer Name* 31-Aug-2004

SHEET 66 OF 69

1

2

3

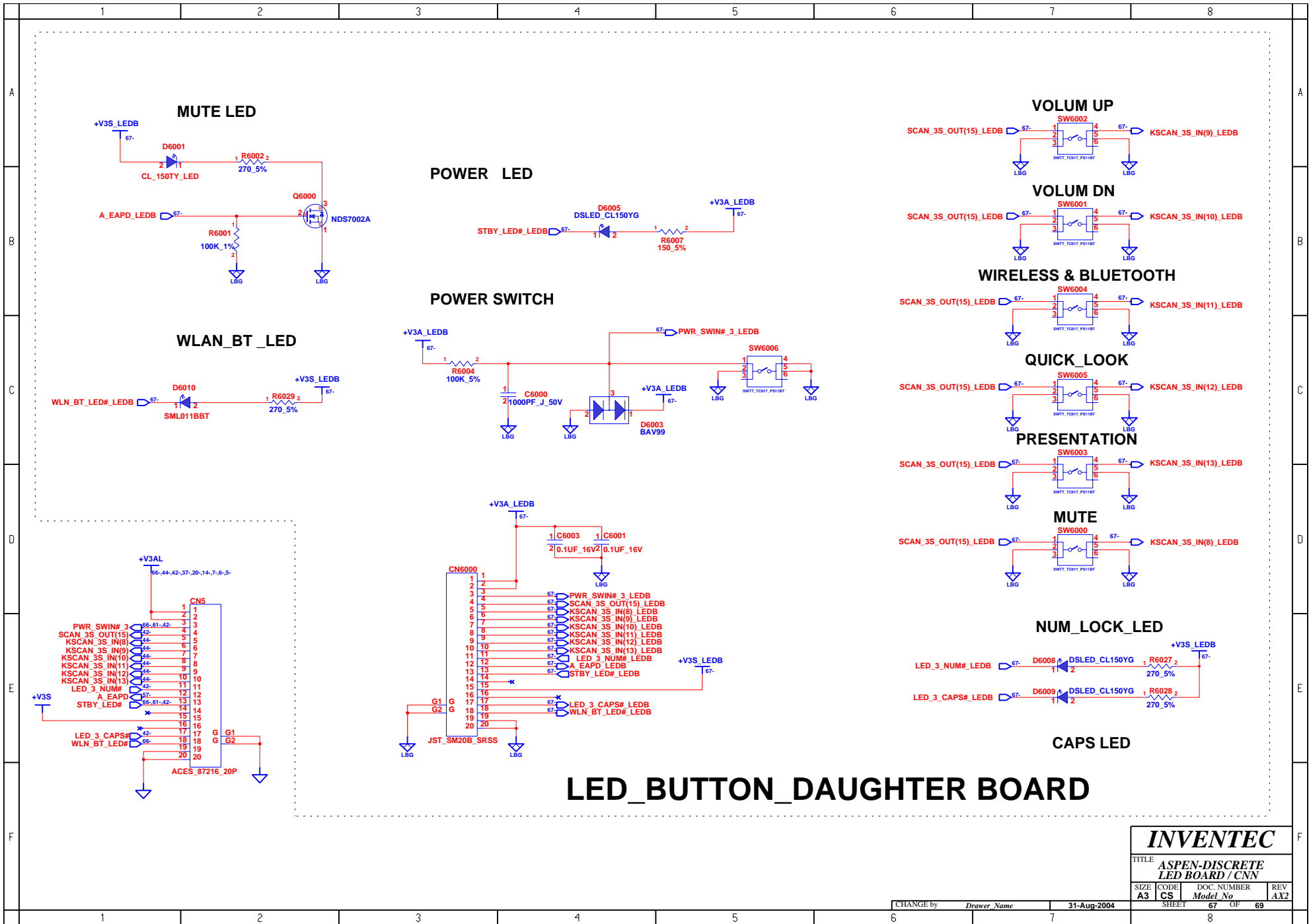
4

5

6

7

8



[illegible]

IEC / HP Aspen_Discrete SI-2 Schematic Changes

September 17, 2004

HP Confidential Information

Open Items 62

Item #	Project	Component / Subsection	PG. #	Description of Requested Change	Date Open+F7 / Closed	Requested By / Owner	Status / Aspen	Aspen FBK/Date
1	Discrete	Charger	5	1.Change R4063 from 162k to 100k, same as Davos design 2.Change R4064 from 221 ohm to 909 ohm, same as Davos design 3.Change R6347 from 47k to 10k, same as Davos design 4.Added a 13.3k ohm series resistor (R6396) between +VADP and R6322 for travel battery may leakage to VADP 5.Changed R6322 to 86.6k and connection on R6396 , U5020 pin5 for travel battery may leakage to VADP 6.Added D6051 in parallel connection R6396 for travel battery may leakage to VADP 7.Added C6167 across D6051 pin2 to GND for travel battery leakage to VADP 8.Changed C6164,C6128,C6129 to 10uf_25V_1206 for saving space 9.Changed C6149,C6150 to 10uf_25V_1206 for saving space 10.Added C6168 across U5026 pin2 to GND.	2004/7/22	IEC	Open	sch0722 done.
2	Discrete	Battery select	6	1.Delete D6035 2.NI R6284 3.Connect R6397.1 to +VADP. 4.Add D6052 across Q6044 source to gate 5.Delete R6034 for saving space	2004/7/22	DB	Open	sch0722 done.
3	Discrete	3V/5V	7	1.Change C6023 value from 0.01uf to 0.1uf 2.Change R6374 value from 10k to 100k 3.Change R6375 value from 10k to 64.9k 4.Change C6160 value from 4.7uf to 22uf 5.Change L33 from PLFC1055P_6R8 to PLFC1055P_3R3 6.Connect R6097pin2 , R6098 pin2 to U32 pin24.	2004/7/22	IEC	Open	sch0722 done.
4	Discrete	2.5L/1.2L	8	Change C519, C520 to 10uF/25V_1206 for saving space.	2004/7/22	IEC	Open	sch0722 done.
5	Discrete	VGAVCC/1.5A	9	Change C561,C4067 to 10uF/25V_1206 for saving space.	2004/7/22	IEC	Open	sch0722 done.
6	Discrete	VCCP/V1.8	10	Change C790, C810 to 10uF/25V_1206 for saving space	2004/7/22	IEC	Open	sch0722 done.
7	Discrete	M24	29,42	for ATI VARI_Bright feature 1.Add R6389 (0 ohm) across U14 pinAG12 to LCM_3S_BKLTEN 2.Add R6390(NI) across U14 pinAG12 to INV_PWM_3 3.Add R6391(NI) across U14 pinAJ5 to LCM_3S_BKLTEN 4.Add R6392(0 ohm jumper) between INV_PWM_3 and U26 pin 96 at page42 (KBC)	2004/7/22	RL	Open	sch0722 done.
8	Discrete	M24	29	Change R180,R181,R179,R583 from 150_1% to 100_1% for DVI, same as the Vail schematic.	2004/7/22	IEC	Open	sch0722 done.

9	Discrete	CRT_SVEDIO	35	1.Add R6394(0 ohm) resistor at U500.20 and CRT_BUF_HSYNC 2.Add R6395(0 ohm) resistor at U500.22 & CRT_BUF_VSYNC. 3.Add NI capacitor, pin1 to CRT_BUF_HSYNC & pin2 to GND. 4.Add NI capacitor, pin1 to CRT_BUF_VSYNC & pin2 to GND. 5.Change C4, C5 & C14 to NI. 6.Change C2, C17 & C500 to 270pF. 7.Change L500, L501 & L502 to 2.2uH_0805.	2004/7/22	MM	Open	sch0722 done.
10	Discrete	NIC transformer	60	Change L504 from LG_2402S to LG_2408S for saving space	2004/7/22	IEC	Open	sch0722 done.
11	Discrete	VCC_CORE	11	Changed R6129 to 69.8k for B1-QS Dothan in C4 state	2004/7/29	DB	Open	sch0729 done.
12	Discrete	M22	29~31	Change ATI parts name from M24 to M22	2004/7/29	IEC	Open	sch0729 done.
12	Discrete	CRT_SVEDIO	35	Change C4, C5, C14, C6165, C6166 to NI	2004/7/29	MM	Open	sch0729 done.
14	Discrete	ICH6	38	Change R6093 to 0 ohm and uninstall R6092 for low power NIC	2004/7/29	RK	Open	sch0729 done.
15	Discrete	AC97	48	Change L5012 from ferrite beads to filter and Moved C4049,C4048,C4047 to L5012 pin2	2004/7/29	IEC	Open	sch0729 done.
16	Discrete	NIC	59	1. Install R6358,R6367,R6359 and open R6360 for low power NIC 2. Change R6082 from 100 ohm to 0 ohm for low power NIC	2004/7/29	RK	Open	sch0729 done.
17	Discrete	NIC	60	Change U1 pin1 connection from SLP_S5#_3R to SLP_S3#_3R for WOL on DC will not be supported	2004/7/29	RL	Open	sch0729 done.
18	Discrete	ICH6	38 39	for NIC LED circuitry -Change U34 pin C25 from LED_LANLINK#_DOCK to LED_LANLINK#. -Change R801 pin1 from LED_LANLINK#_DOCK to LED_LANLINK#.	2004/8/4	RK	Open	sch0809 done
19	Discrete	NIC	59	Connect the NIC controller LED signals directly to the dock LEDs 1.Connect R58 pin1 to Q25 pin5 by LED_LANLINK# signal 2.Connect R60 pin1 to Q25 pin2 by LED_LANACT# signal	2004/8/4	RK	Open	sch0809 done
20	Discrete	USB	53	Add a 0 ohm between CN17-G1,G2 and DGND for EMI	2004/8/4	IEC_EMI	Open	sch0809 done
21	Discrete	RJ45 CONN	60	Change CN6 from TYCO to FOX , same as UMA.	2004/8/5	IEC	Open	sch0809 done
22	Discrete	NIC	60	for WOL on DC S3 mode will not be supported 1.Open L5020, R6361 2.Install U1,U2,Q1,Q508,L12,R28,R516 3.Change U1-5,U2-5,R6361-1 from +V3A to +V3AL 4. Add a serial 0 ohm(R6405) at SLP_S3#_3R line and optional a jumper(R6406) between on U1-1 and SLP_S5#_3R line	2004/8/5	IEC	Open	sch0809 done
23	Discrete	LED	66	1. Change battery-charge led from dual led to two single led for no dual led in Amber/Green. 2. Change R352 from 10K to 100K.	2004/8/5	IEC	Open	sch0809 done
24	Discrete	SUSPEND_LED	67	Change Q6014-1 from +V3A_LEDB to +V3S_LEDB, same as UMA	2004/8/5	HP	Open	sch0809 done
25	Discrete	KBC	42	Add a (R6402) 10k pull-up to +V3AL from STBY_LED#, same as UMA	2004/8/9	HP	Open	sch0809 done
26	Discrete	ICH6	37	Add two 0 ohm(R6400,R6399) to DGND from U34 pin AC1, AC2, same as UMA	2004/8/9	HP	Open	sch0809 done

27	Discrete	CRT_SVEDIO	35	Add a layout note on D1,D501,D2 close to SVIDEO CONN	2004/8/9	IEC	Open	sch0809 done
28	Discrete	ICH6	38 39	Add a test point(TP734) on TI_GRST# signal (U34-T3), same as UMA Add a test point(TP735) on IDE_RESET# signal (U34-E7), same as UMA	2004/8/9	HP	Open	sch0809 done
29	Discrete	ICH6	40	Add a 1_1% resistor(R6401) on +V1.5S and L30-1, same as UMA	2004/8/9	IEC	Open	sch0809 done
30	Discrete	LCD_CONN	36	Change C25 from 4700pf to 0.1uf_25V (0603), same as UMA	2004/8/9	IEC	Open	sch0809 done
31	Discrete	ICH6	38	Install R6092 to 10K, same as UMA	2004/8/9	IEC	Open	sch0809 done
32	Discrete	NIC	59	Not Install R6367, same as UMA	2004/8/9	IEC	Open	sch0809 done
33	Discrete	LED	66	Change R10 from 270k to 10k, same as UMA	2004/8/9	IEC	Open	sch0809 done
34	Discrete	FWH	43	Remove R6357 at CN4005 pin 29 , same as UMA	2004/8/9	IEC	Open	sch0809 done
35	Discrete	Charger	5	1. Remove D6051, C6167. 2.Add R6407_150K between U5020 pin7 (ADP_PRES) and U5021 pin1. 3Add D6056_1N4148 between U5021 pin1 and U5026 pin1.	2004/8/18	IEC	Open	sch0818 done
36	Discrete	3V/5V	7	Replace D6045_DAN202K to U5036_TC7S32F	2004/8/18	IEC	Open	sch0818 done
37	Discrete	VGA_VCC/1.5A	9	for M22 power play on 1V/1.15V 1.Change R6162 from 11.3K to 10.2K. 2.Change R160 from 10.2K to 10K. 3.Change R6163 from 36.5K to 34K.	2004/8/18	RL	Open	sch0818 done
38	Discrete	M22	29	for ENA_BL from GPIO(0) to GPIO(7) 1.Change R6391 pin2 from VGA_GPIO(0) to VGA_GPIO(7).	2004/8/18	MM	Open	sch0818 done
39	Discrete	NIC	38 59	for NIC Low Power circuit -Not install R6092. -Install R6367 (10K).	2004/8/18	RK	Open	sch0818 done
40	Discrete	PC card slot	55	SmartCard power for EMV test, same as Vail 1.Replace Q44(FDC638P) to U5038 (TPS2061DGN) 2.Add a R6408 (open) for TPS2065DGN with active high enable. 3.Remove R1, C33. 4.Change R3 from 470K to 10K	2004/8/18	HP	Open	sch0818 done
41	Discrete	New power button	38 42 66 67	Remove Quick look led and suspend led for new ME ID -Remove standby_sw# at U34 pin C23 -Change U26 pin62 from PWR_SWIN1#_3 to PWR_SWIN#_3 -Change D29 from dual led to single led (green color) for power led -Remove Q37,R352,Q40,R355,Q38,Q13, R10, U5029,Q7,R6363 -Change R359 from 270 to 150 -Change D6005 from dual led to single led -Remove Q6002,R6003,Q6001 -Remove D6002,R6006,R6090,Q6014 -Change SW6006 from power switch to power button -Remove R6005,C6002,D6004 -Change CN5 pin14 and CN6000 pin 14 to NC pin	2004/8/18	ME	Open	sch0818 done
42	Discrete	3V/5V	7	Not Install R6378	2004/8/19	IEC	Open	sch0819 done
43	Discrete	VCC_CORE	11	To set OCP to 32A: -Change R6122,R6123 from 432_1% to 510_1%.	2004/8/19	IEC	Open	sch0819 done
44	Discrete	VCC_CORE	11	Change D14, D16 from SSM34_3A to NI	2004/8/19	IEC	Open	sch0819 done
45	Discrete	M22	29	Change R180,R181,R179,R583 from 100_1% to 150_1% for DVI, same as SI-1	2004/8/19	MM	Open	sch0819 done
46	Discrete	CRT_SVEDIO	35	Replace R6394,R6395 to L23, L39 (FBM_10_160808_800T)	2004/8/19	MM	Open	sch0819 done

47	Discrete	3V/5V	7	Remove R6378	2004/8/23	IEC	Open	sch0823 done
48	Discrete	VRAM configure	29,38	Modify the VRAM configure table on sheet29 and Remove the table on sheet38, same as Vail	2004/8/23	HP	Open	sch0823 done
49	Discrete	LCD_CONN	36	Add a buffer between INV_PWM_3 line and CN3 pin 36	2004/8/23	IEC	Open	sch0823 done
50	Discrete	Quick_Lock	45 67	Remove Quick_Lock signal -Change U8 pin32 from QUICK_LOCK# to GP10 -Change RS1 pin6 from QUICK_LOCK# to GP10 -Change CN5 pin16 from QUICK_LOCK# to NC -Change CN6000 pin 16 from QUICK_LOCK# to NC	2004/8/23	IEC	Open	sch0823 done
51	Discrete	NIC	60	1. Change U1 pin5 and U2 pin5 from +V3AL to +V3A for WOL on DC will not be supported, same as Vail. 2. Add a 220K ohm serial resister between ADP_PRES line and U1 pin2	2004/8/23	HP	Open	sch0823 done
52	Discrete	Charger	5	Change the charge current from 2A to 3A, same as Davs. -Change R6324 from 191K_1% to 140K_1%. -Change R6264 from 140K_1% to 191K_1%.	2004/9/7	HP	Open	sch0913 done
53	Discrete	+V1.5A discharge	7 9	Add a discharge circuit on +V1.5A -Add a +V1.5A_ON signal at U32 pin 12 and connect a pull up resister 10K to +V3A. -Change Q6018 pin2 from +V3A to +V1.5A_ON. -Add a dual FET (FDG6301N) to do +V1.5A discharge.	2004/9/7	HP	Open	sch0913 done
54	Discrete	VCC_CORE	11	Connect R6135 pin2 to U5008 pin3	2004/9/13	IEC	Open	sch0913 done
55	Discrete	NIC	59	Insatl R531 to 30K when B5751M B1 silcon is installed.	2004/9/13	HP	Open	sch0913 done
56	Discrete	ICH6	38	Remove R272,R276,R736,R283 reserve pads on ICH6 VMEM configuration	2004/9/13	IEC	Open	sch0913 done
57	Discrete	3V/5V	7	Change R337,R351 from 0 ohm to 4.7_5% for EMI request	2004/9/15	EMI	Open	sch0915 done
58	Discrete	VCC_CORE	11	Change R290, R6121 from 2.2_5% to 4.7_5% for EMI request.	2004/9/15	EMI	Open	sch0915 done
59	Discrete	VGAVCC/1.5A	9	Add C6169 100PF_50V between Q6018 drain and U5009 AGND for reducing overshoot/undershoot	2004/9/15	IEC	Open	sch0915 done
60	Discrete	VCCP/V1.8	10	Add C6170 100pf_50V between Q6024 drain and U5013 AGND for reducing overshoot/undershoot	2004/9/15	IEC	Open	sch0915 done
61	Discrete	PC card slot	55	To change the SC_VCC power switch parts from EN# active to EN active -Change U5038 from TPS2061 to TPS2065 -Remove R3, Q42 -Install R6408 to 0 ohm	2004/9/17	IEC	Open	Implement 0917BOM.
62	Discrete	Sleep	13	Auto reboot when perform S4/S5 mode on Aspen discrete and Vail platform. - Change R6387, R6388 from 10K to 470pf to delay SLP_ S3#_5R about 5us on falling time.	2004/9/17	IEC	Open	Implement 0917BOM.